

A

A

B

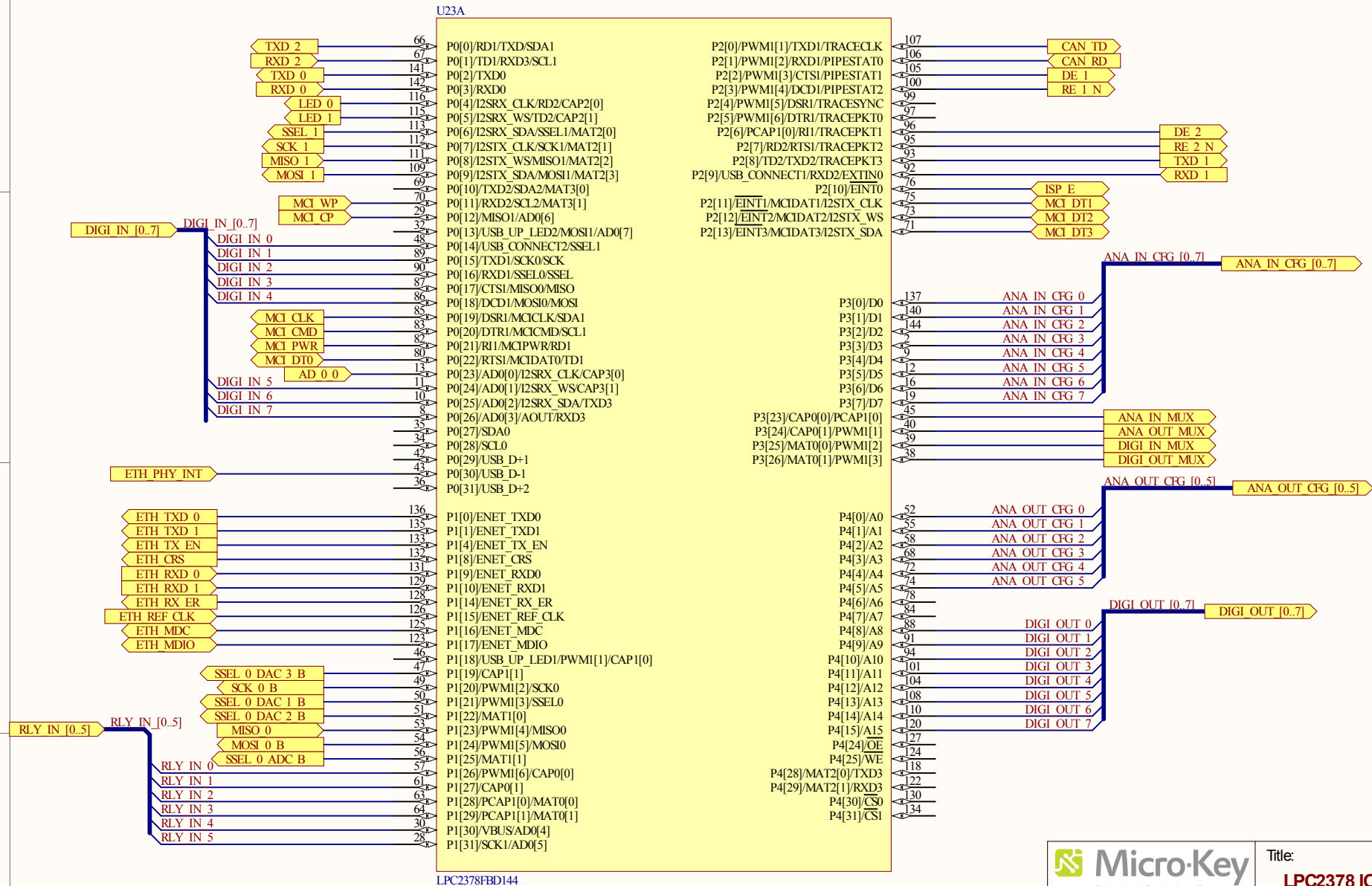
B

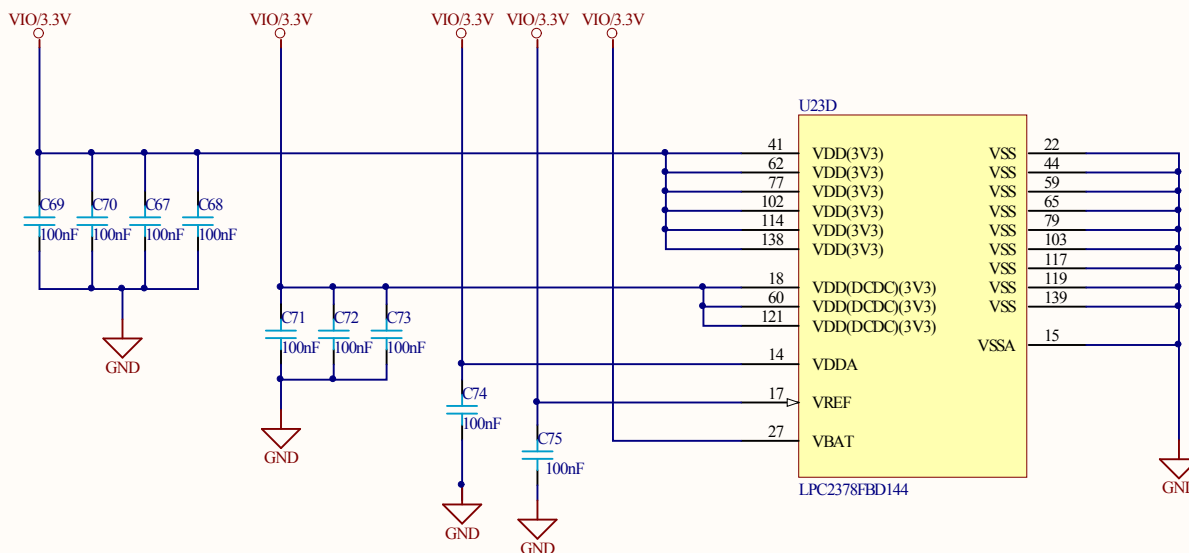
C


C

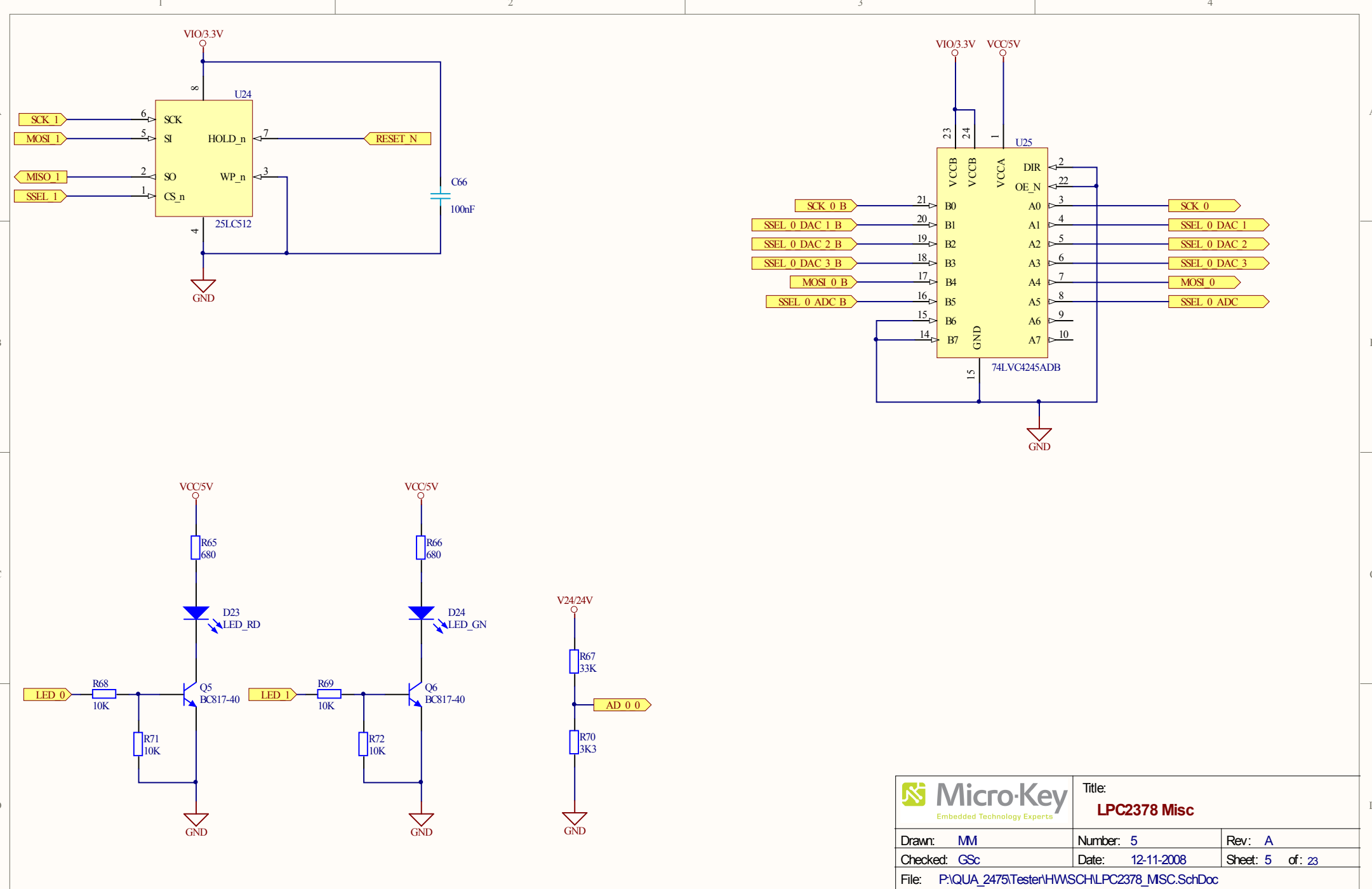
D

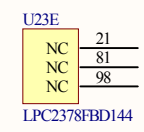
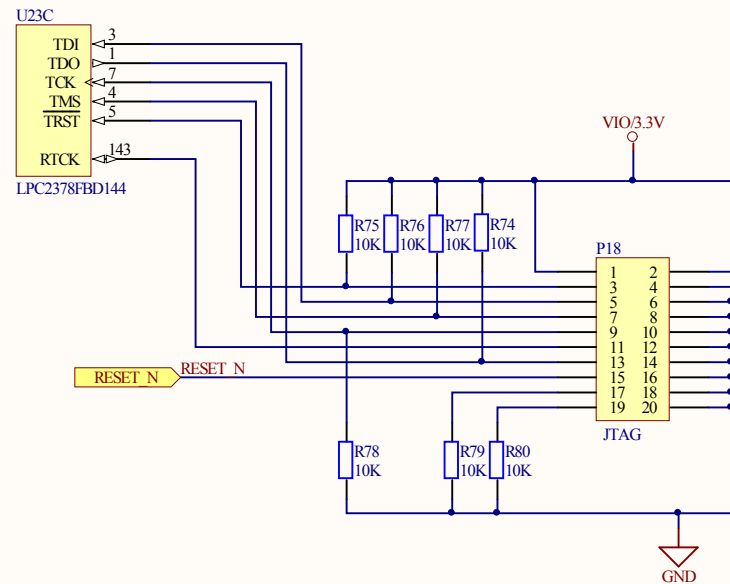
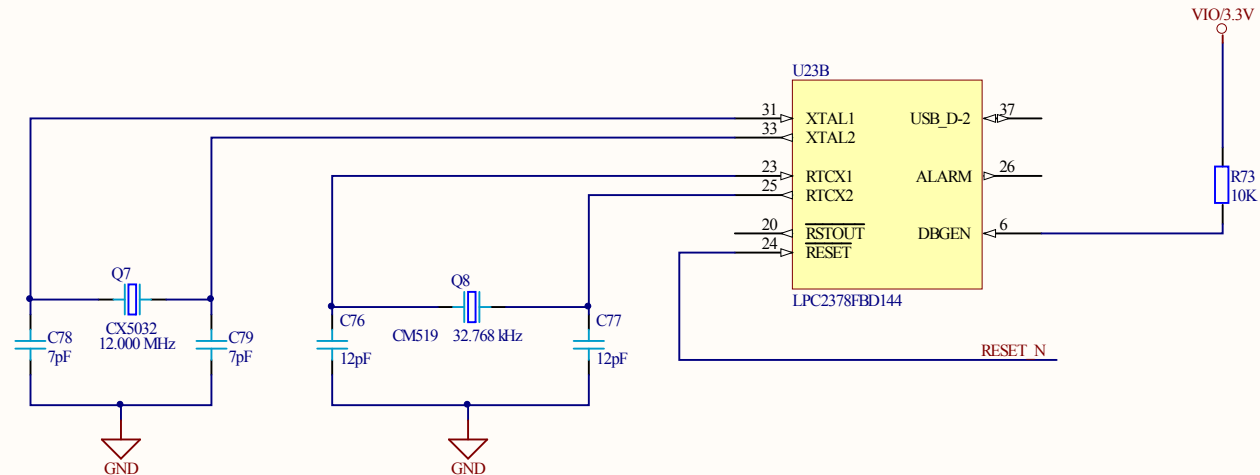
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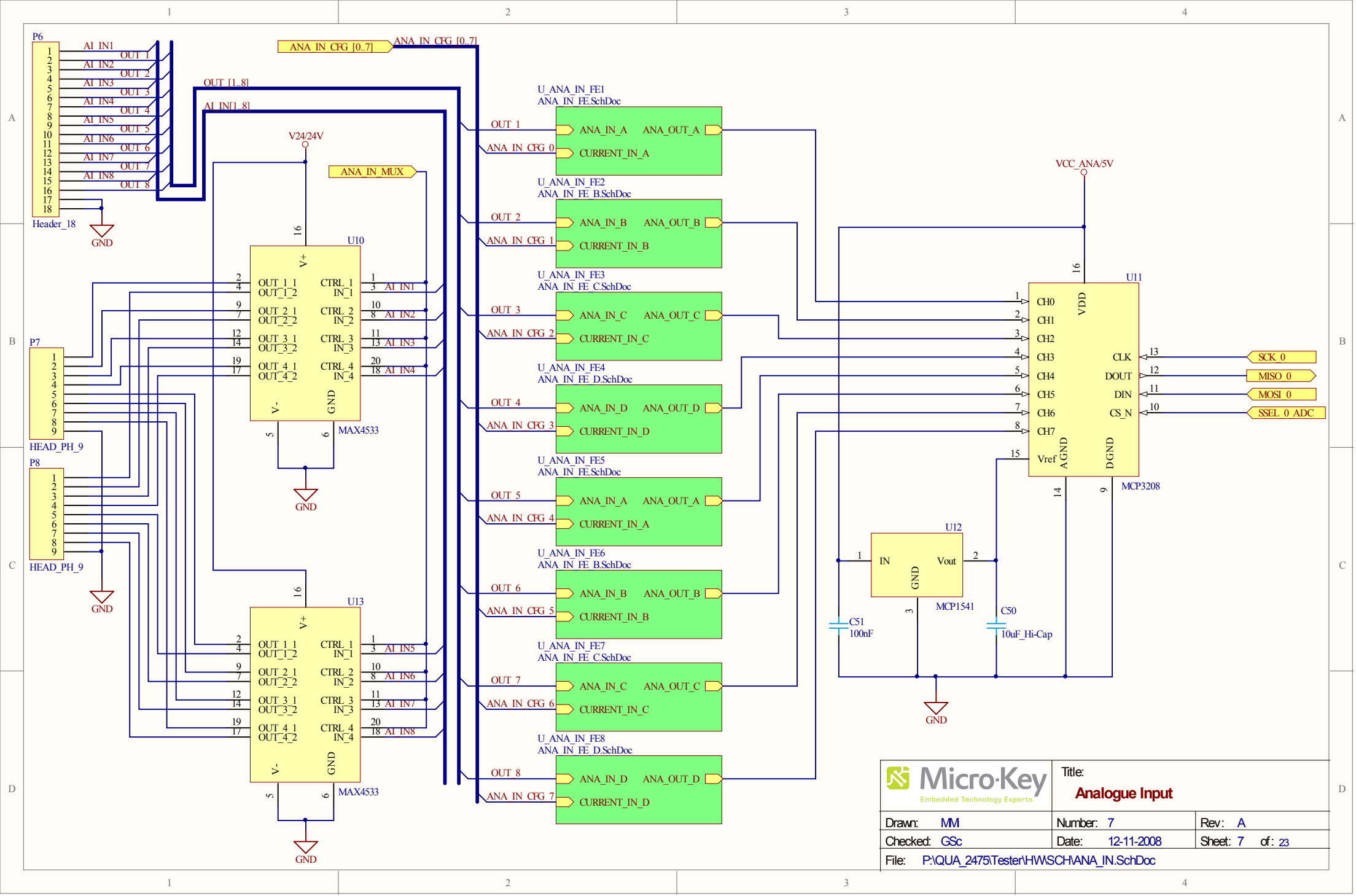


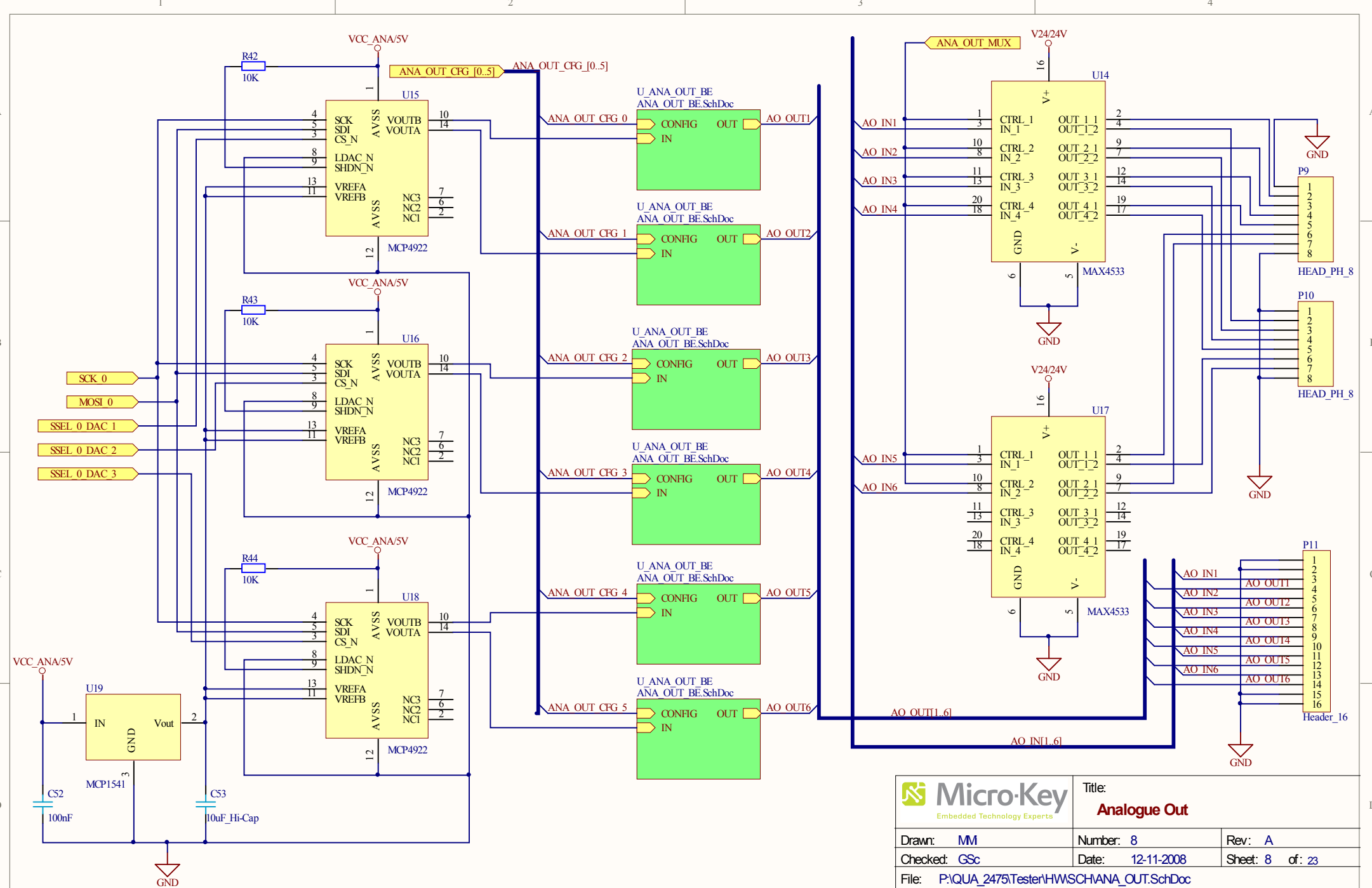
 Micro-Key Embedded Technology Experts		Title: LPC2378 Powersupply	
Drawn: MM	Number: 4	Rev: A	
Checked: GSC	Date: 12-11-2008	Sheet: 4 of: 23	
File: P:\QUA_2475\Tester\HWSCHLPC2378_Power.SchDoc			





 Micro-Key Embedded Technology Experts		Title: LPC2378 SYS	
Drawn: MM	Number: 6	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 6	of: 23
File: P:\QUA_2475\Tester\HMSCH\LPC2378_SYS.SchDoc			





A

A

B

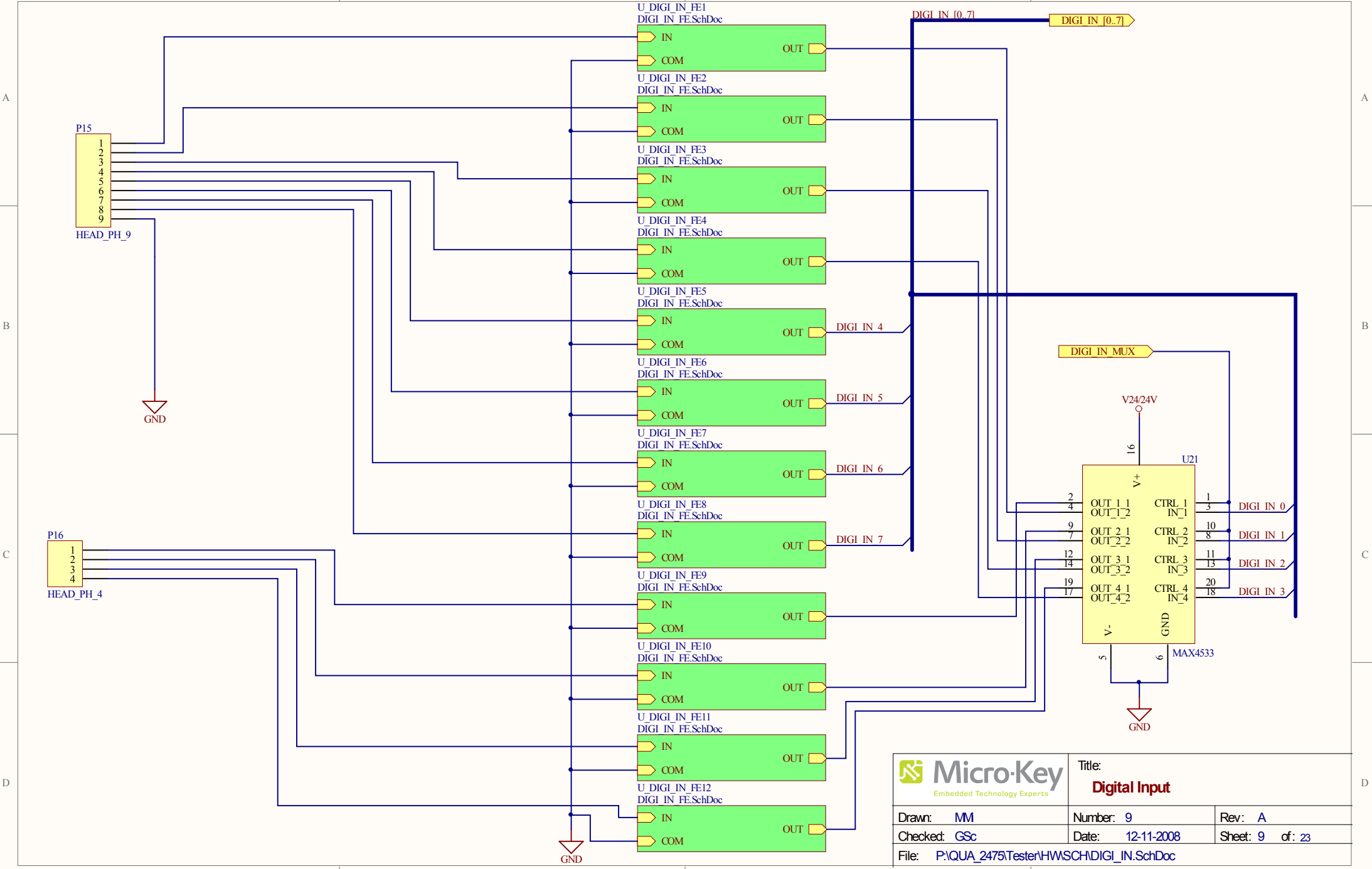
B


C

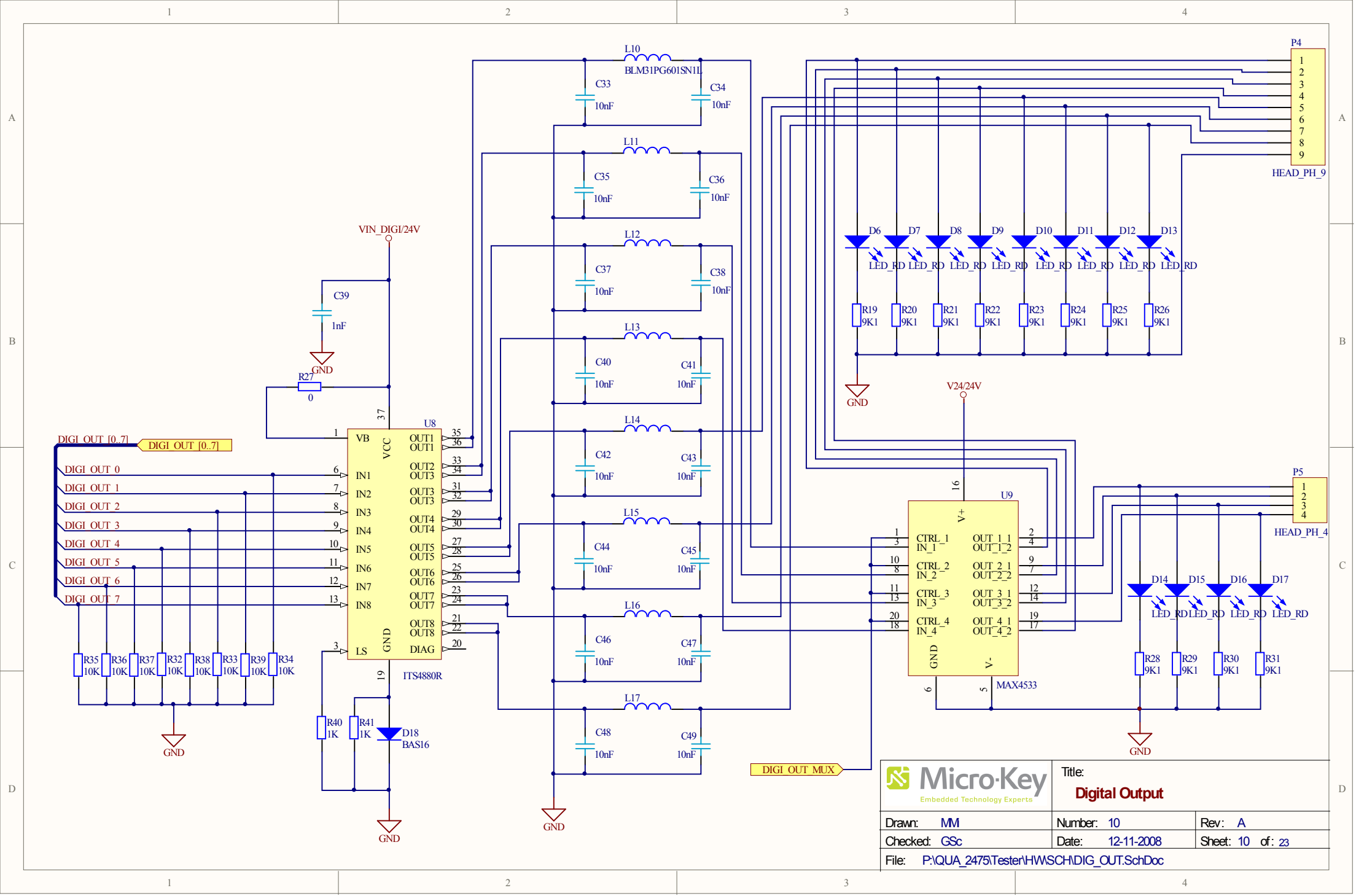
C

D

D



 Micro-Key Embedded Technology Experts		Title: Digital Input	
Drawn: MM	Number: 9	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 9 of: 23	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN.SchDoc			



A

B

C

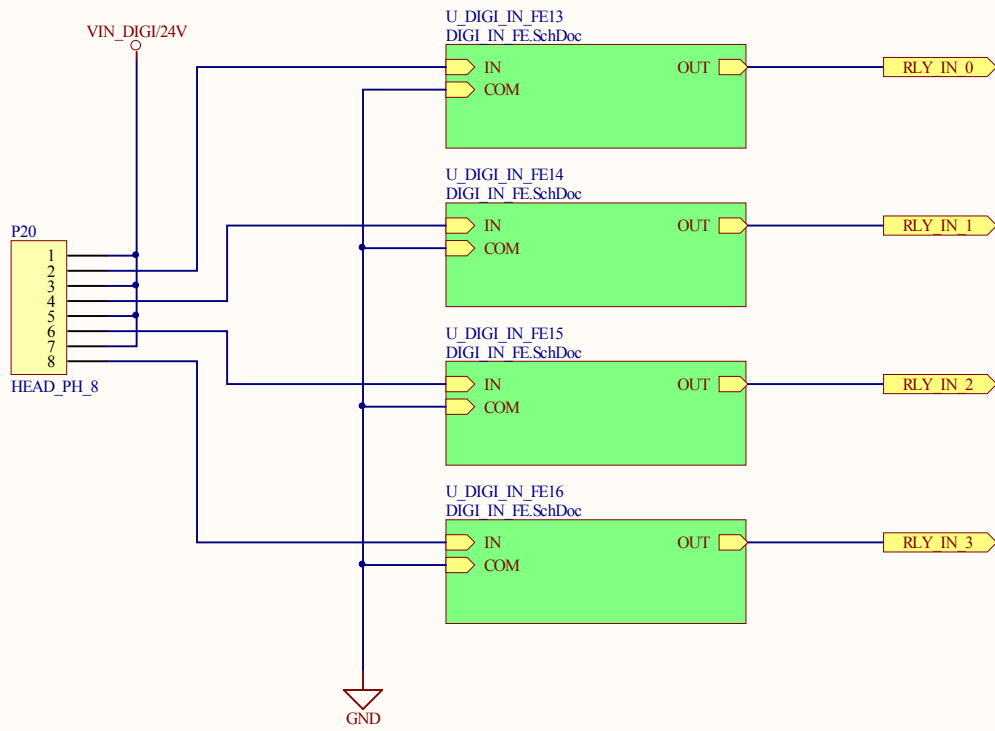
D


A

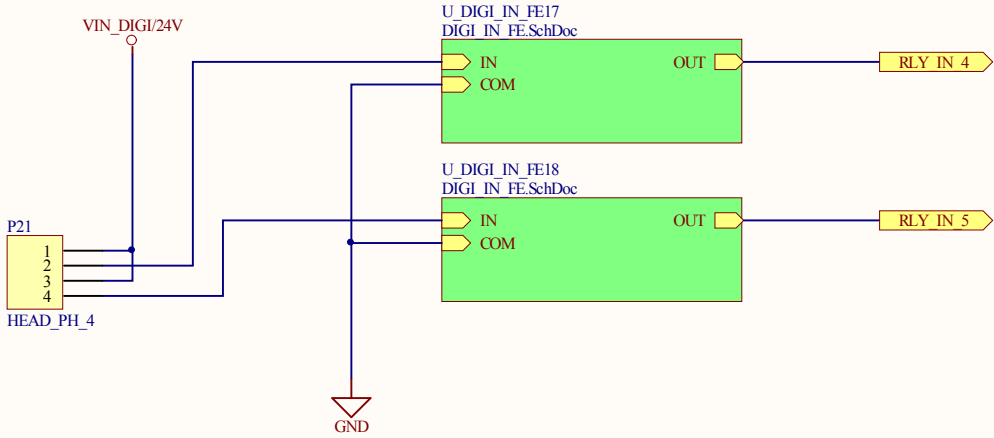
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C

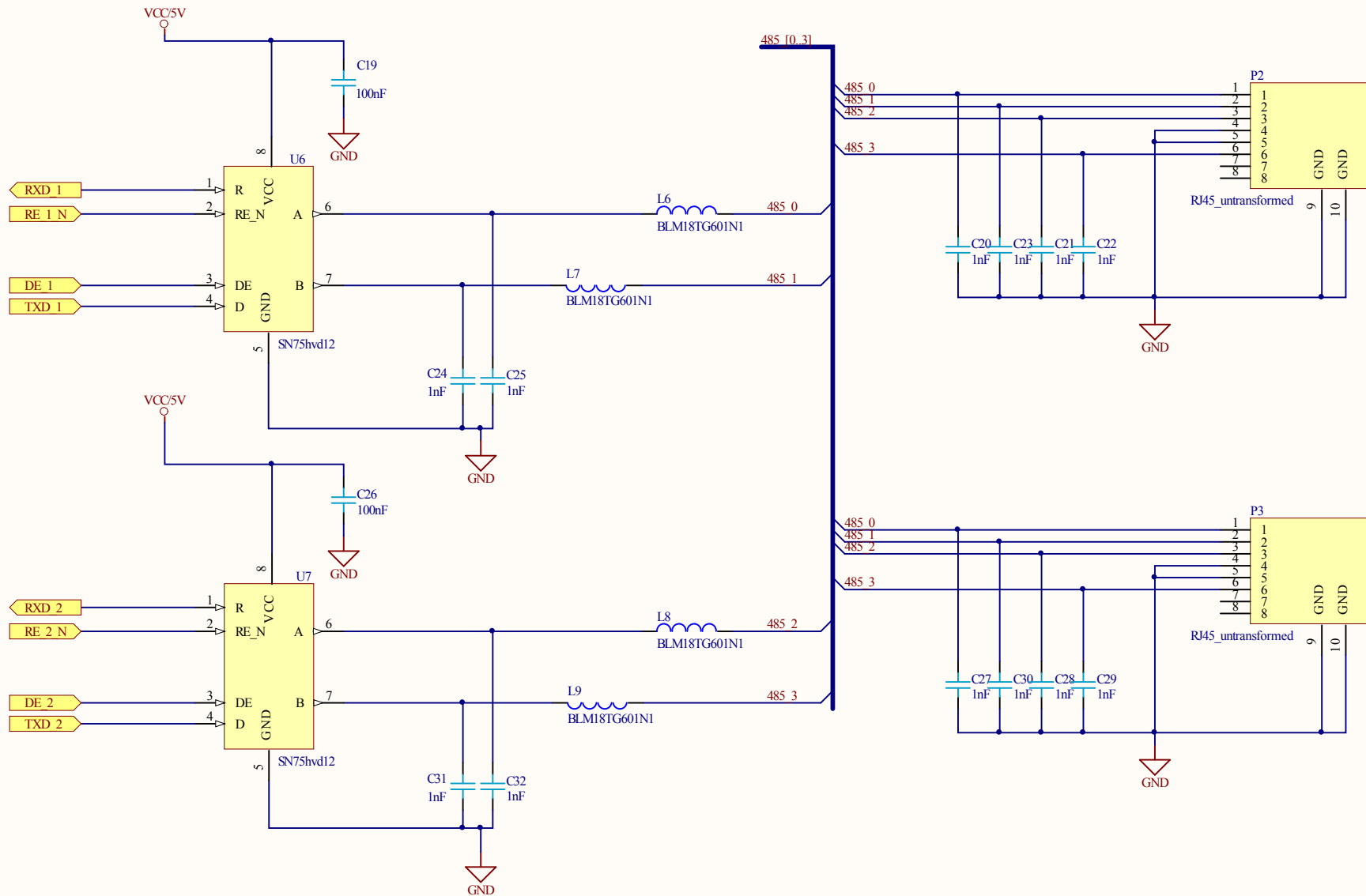
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 Micro-Key Embedded Technology Experts		Title: Relay Input	
Drawn: MM	Number: 11	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 11 of: 23	
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 Micro-Key Embedded Technology Experts		Title: Relay Input EB	
Drawn: MM	Number: 12	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 12 of: 23	
File: P:\QUA_2475\Tester\HWSCHARLY_IN_EB.SchDoc			



Title:
RS 485

Drawn: MM

Number: 14

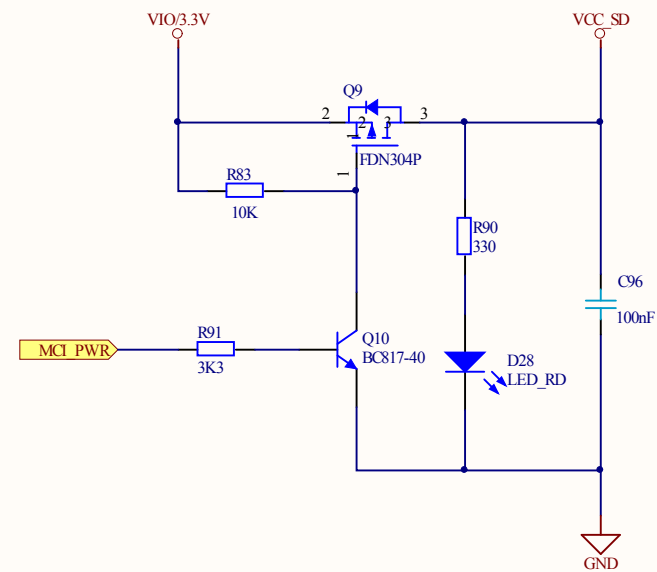
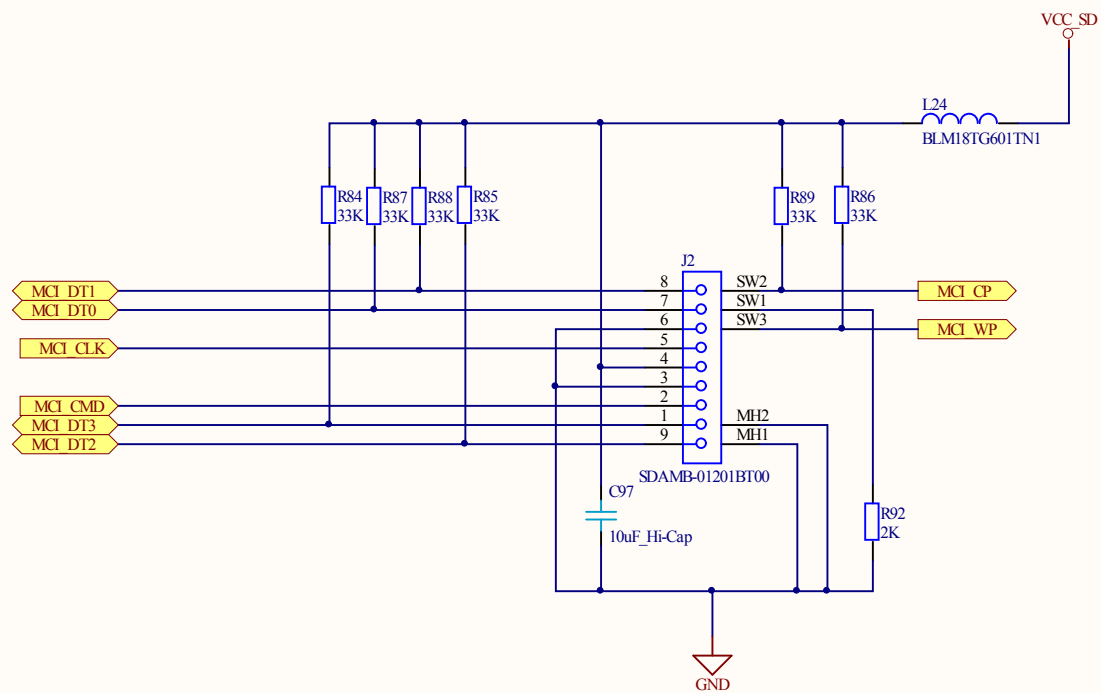
Rev: A

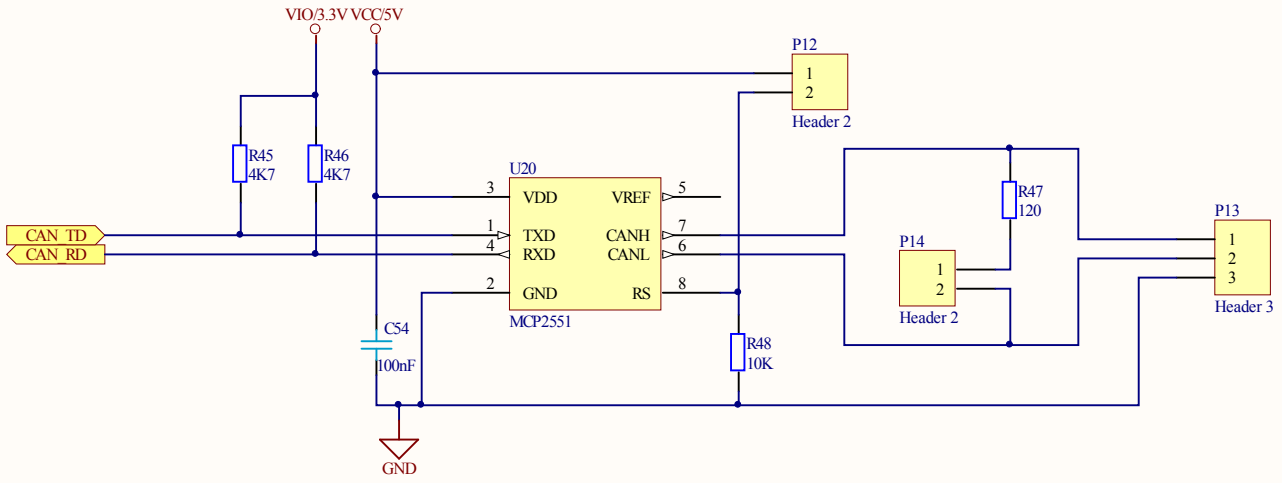
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Date: 12-11-2008

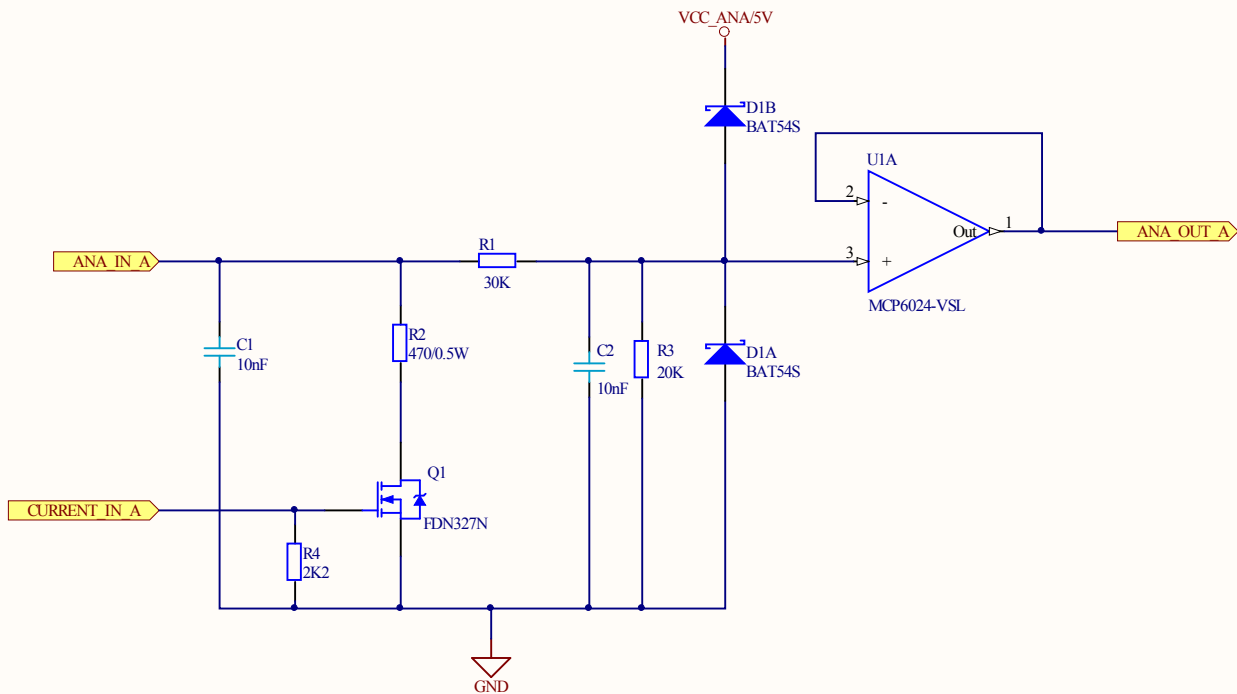
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File: P:\QUA_2475\Tester\HMSCHRS485.SchDoc

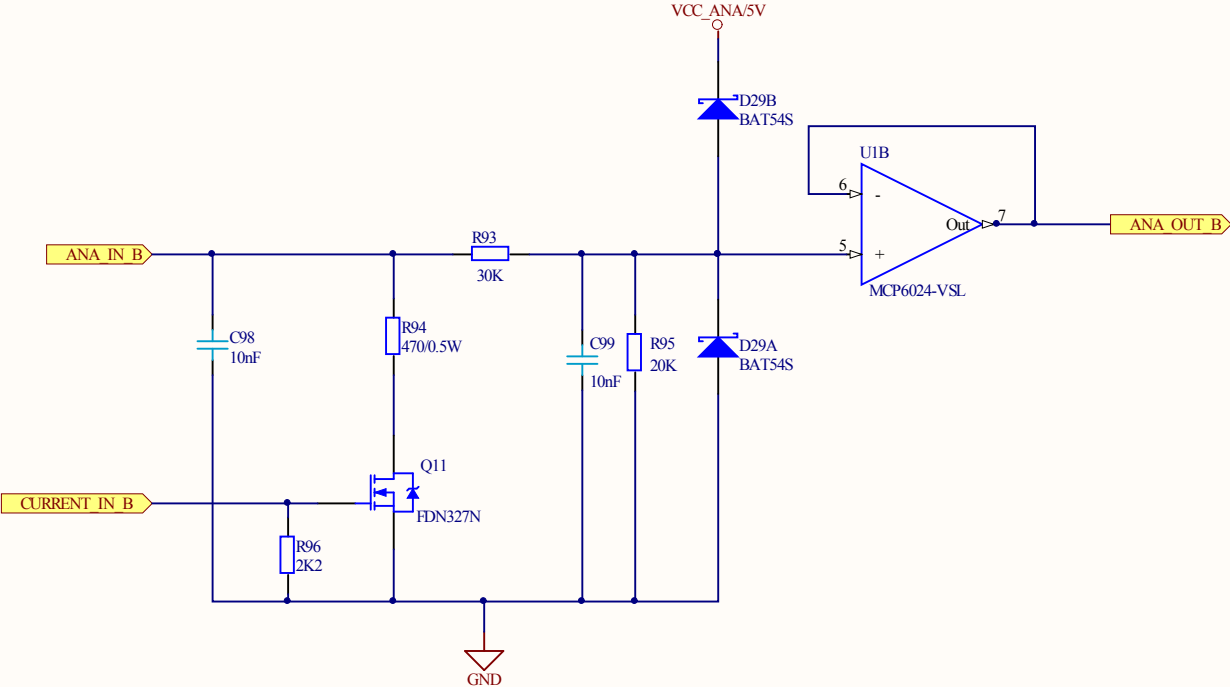




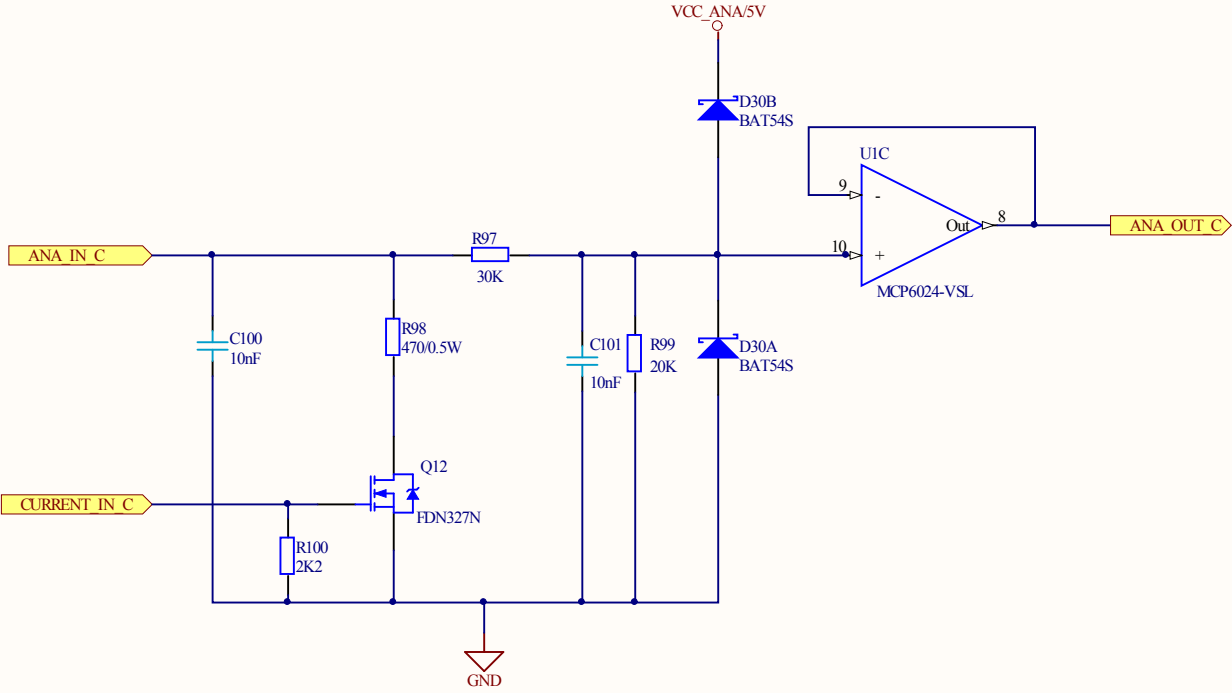
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Drawn: MM	Number: 17	Rev: A
Checked: GSc	Date: 12-11-2008	Sheet: 17 of: 23
File: P:\QUA_2475\Tester\HMSCH\CAN.SchDoc		




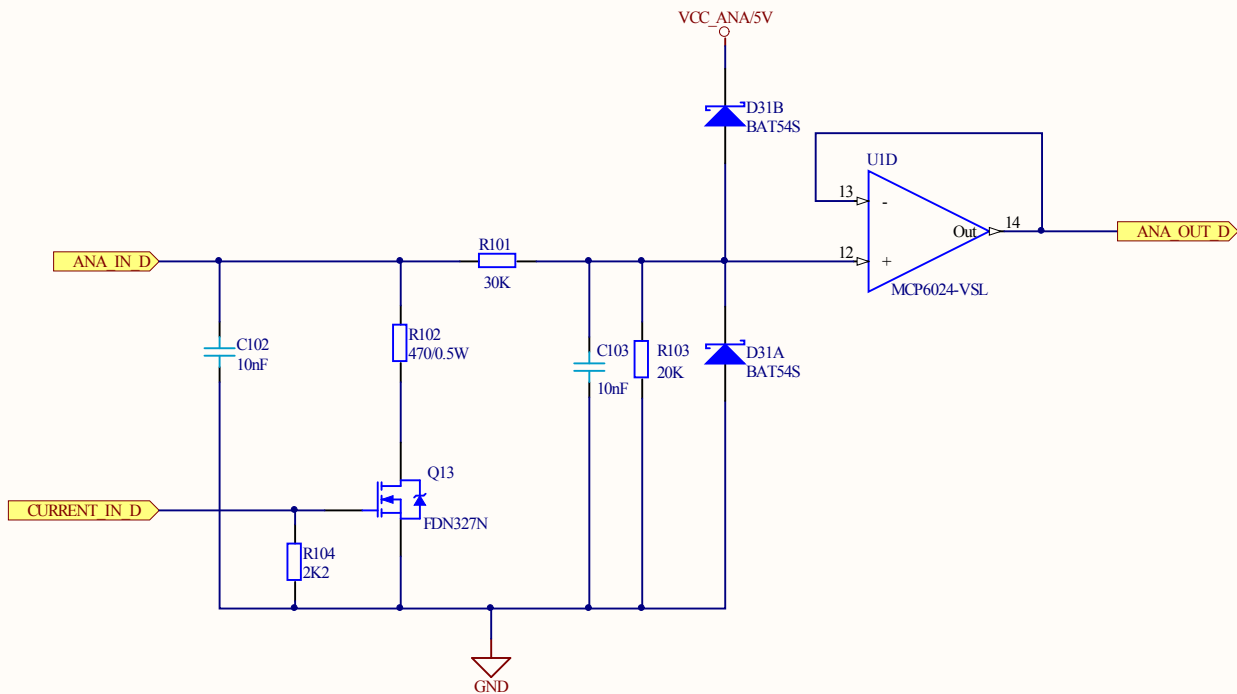
 Micro-Key Embedded Technology Experts		Title: Analogue In FE_A	
Drawn: MM	Number: 18	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 18 of: 23	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE.SchDoc			




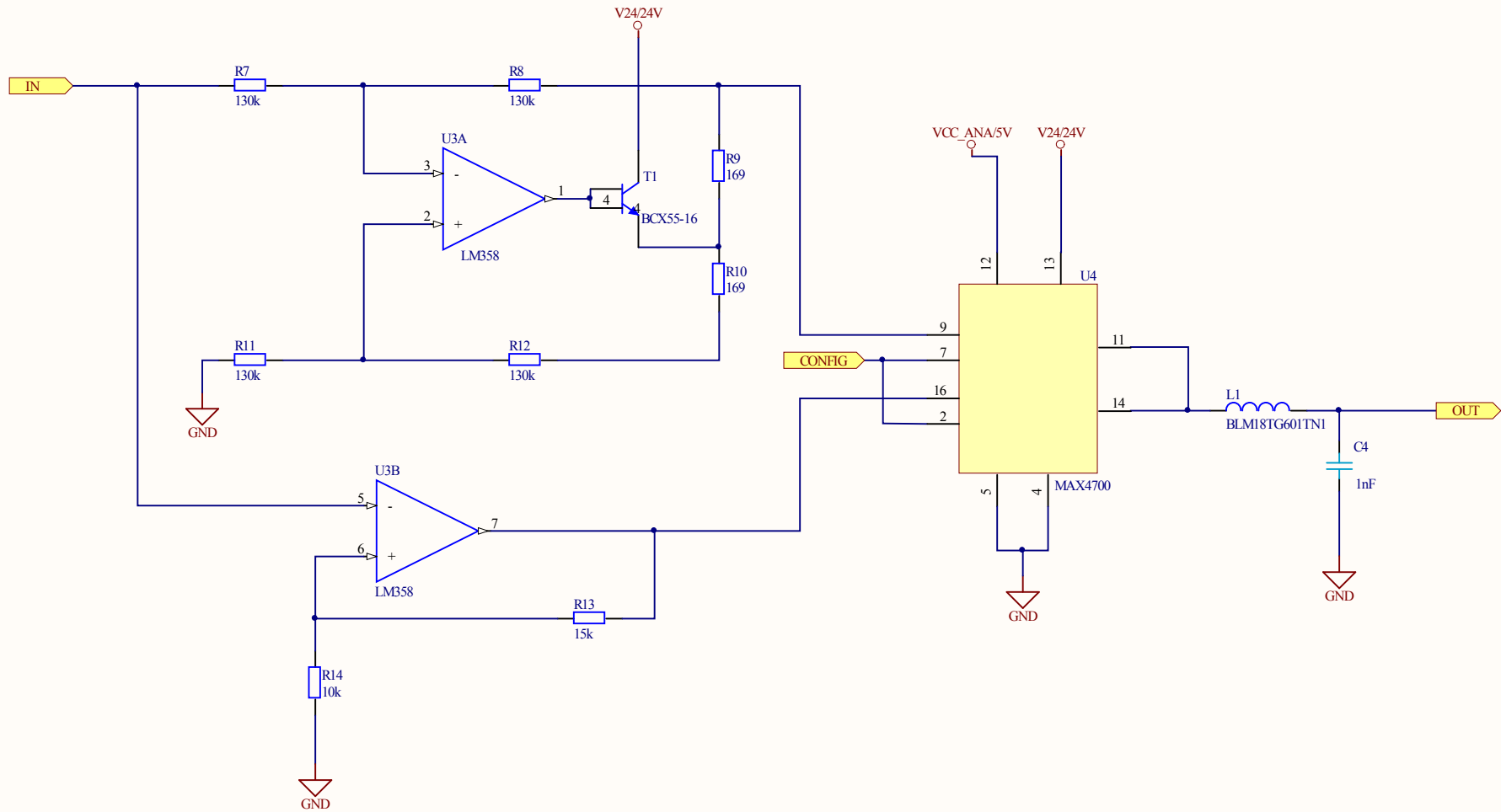
Title: Analogue In FE_B		
Drawn: MM	Number: 19	Rev: A
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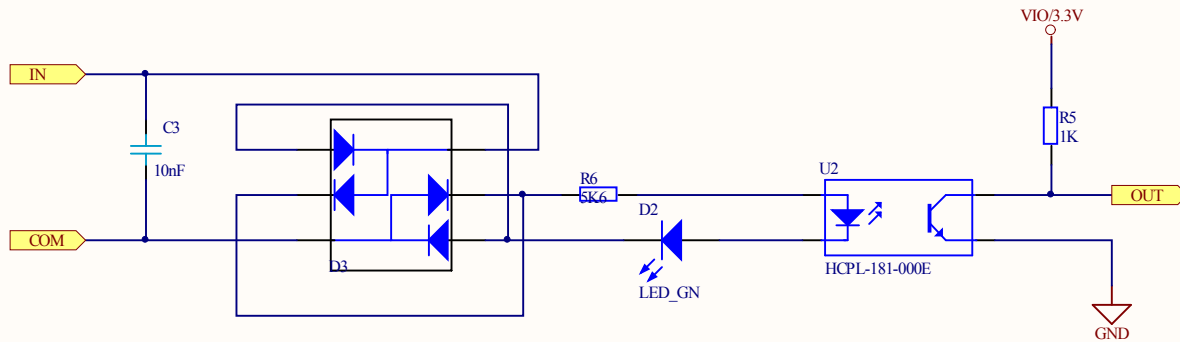
 Micro-Key Embedded Technology Experts		Title: Analogue In FE_C	
Drawn: MM	Number: 20	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 20 of: 23	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_C.SchDoc			




 Embedded Technology Experts	Title: Analogue In FE_D	
Drawn: MM	Number: 21	Rev: A
Checked: GSc	Date: 12-11-2008	Sheet: 21 of: 23
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_D.SchDoc		



 Micro-Key <small>Embedded Technology Experts</small>		Title: Analogue Out BE	
Drawn: *		Number: 22	Rev: *
Checked: *		Date: 12-11-2008	Sheet: 22 of: 23
File: P:\QUA_2475\Tester\HMSCHANA_OUT_BE.SchDoc			



 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 23	Rev: A	
Checked: GSc	Date: 12-11-2008	Sheet: 23 of: 23	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			