

A

A

B

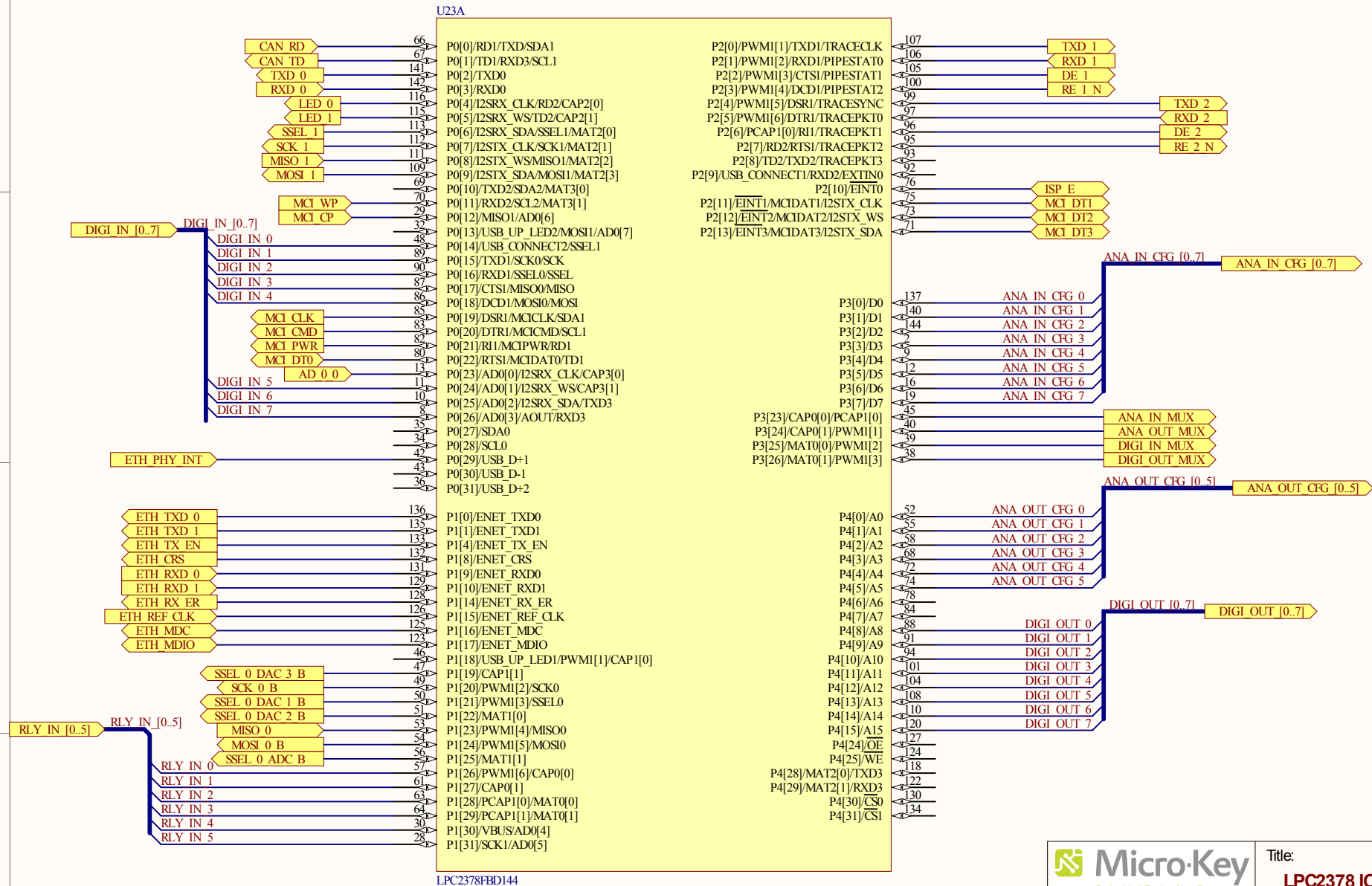
B


C

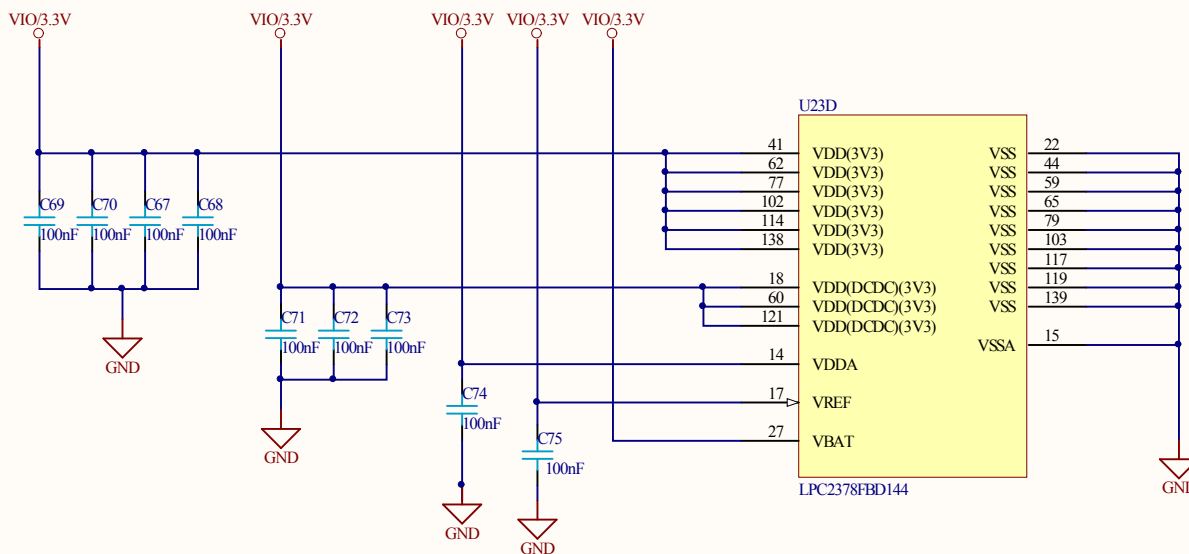
C


D

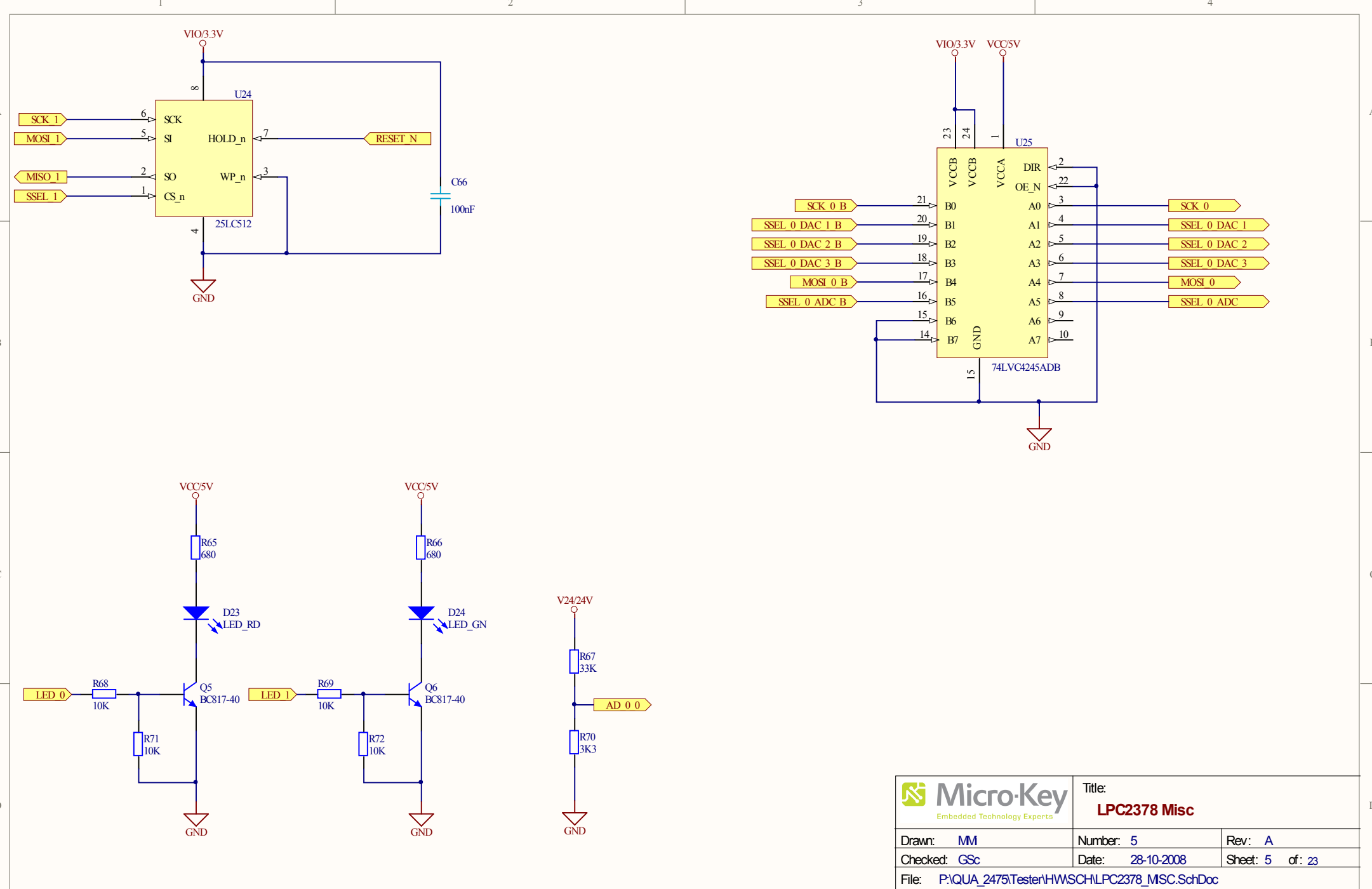
D

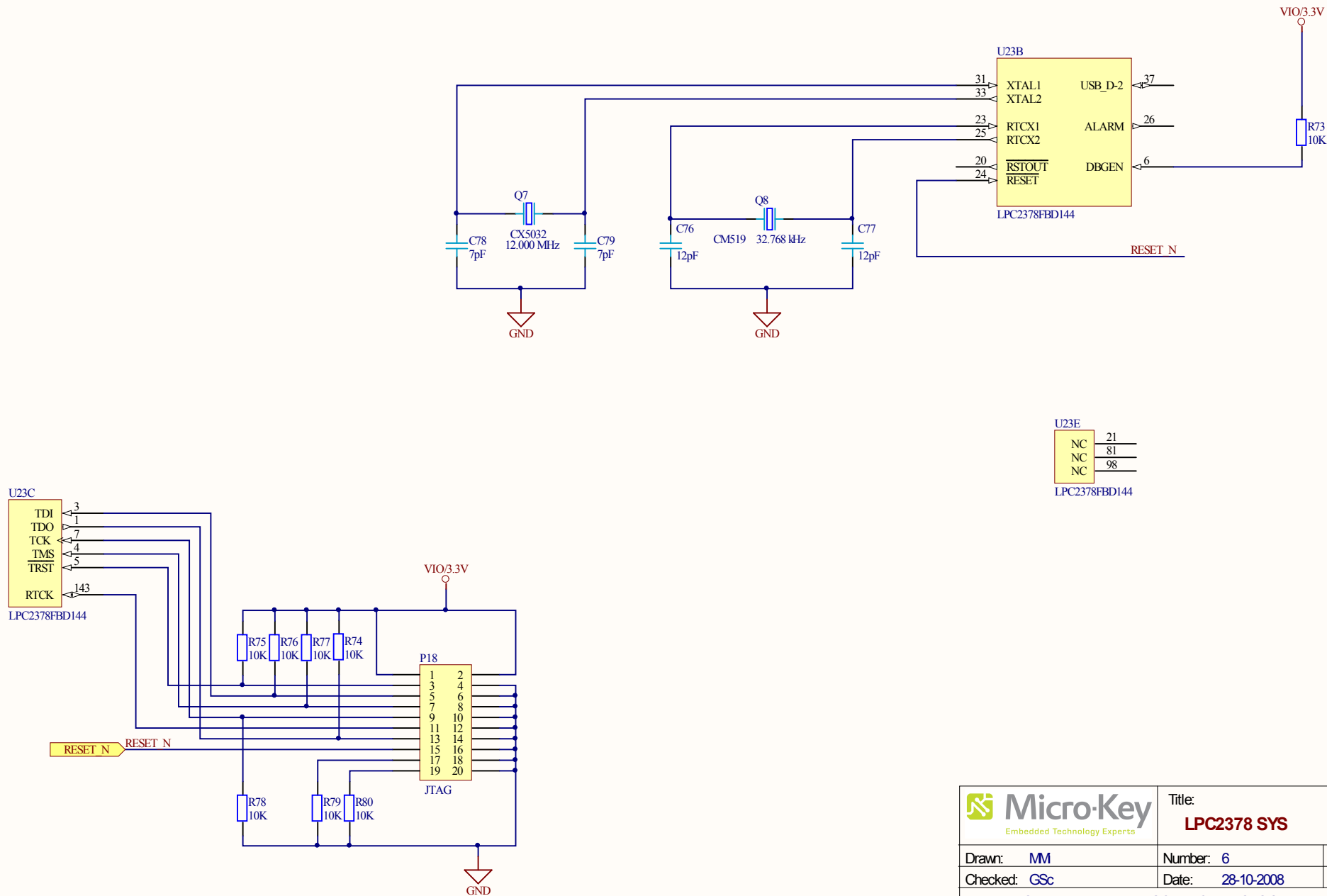



 Micro-Key Embedded Technology Experts		Title: LPC2378 IO	
Drawn: MM	Number: 3	Rev: A	
Checked: GSc	Date: 28-10-2008	Sheet: 3 of: 23	
File: P:\QUA_2475\Tester\HWSCHN\LPC2378_IO.SchDoc			

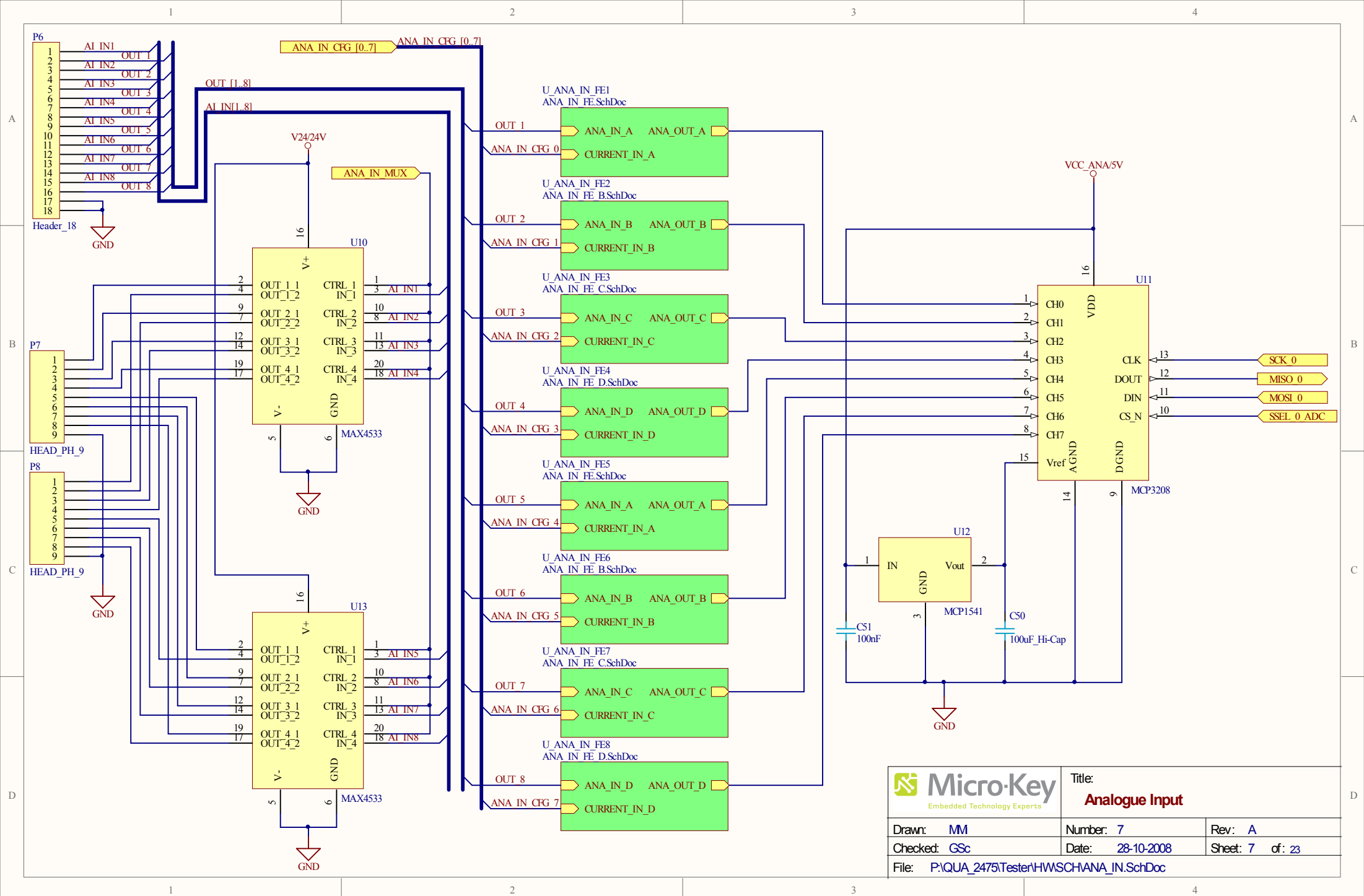


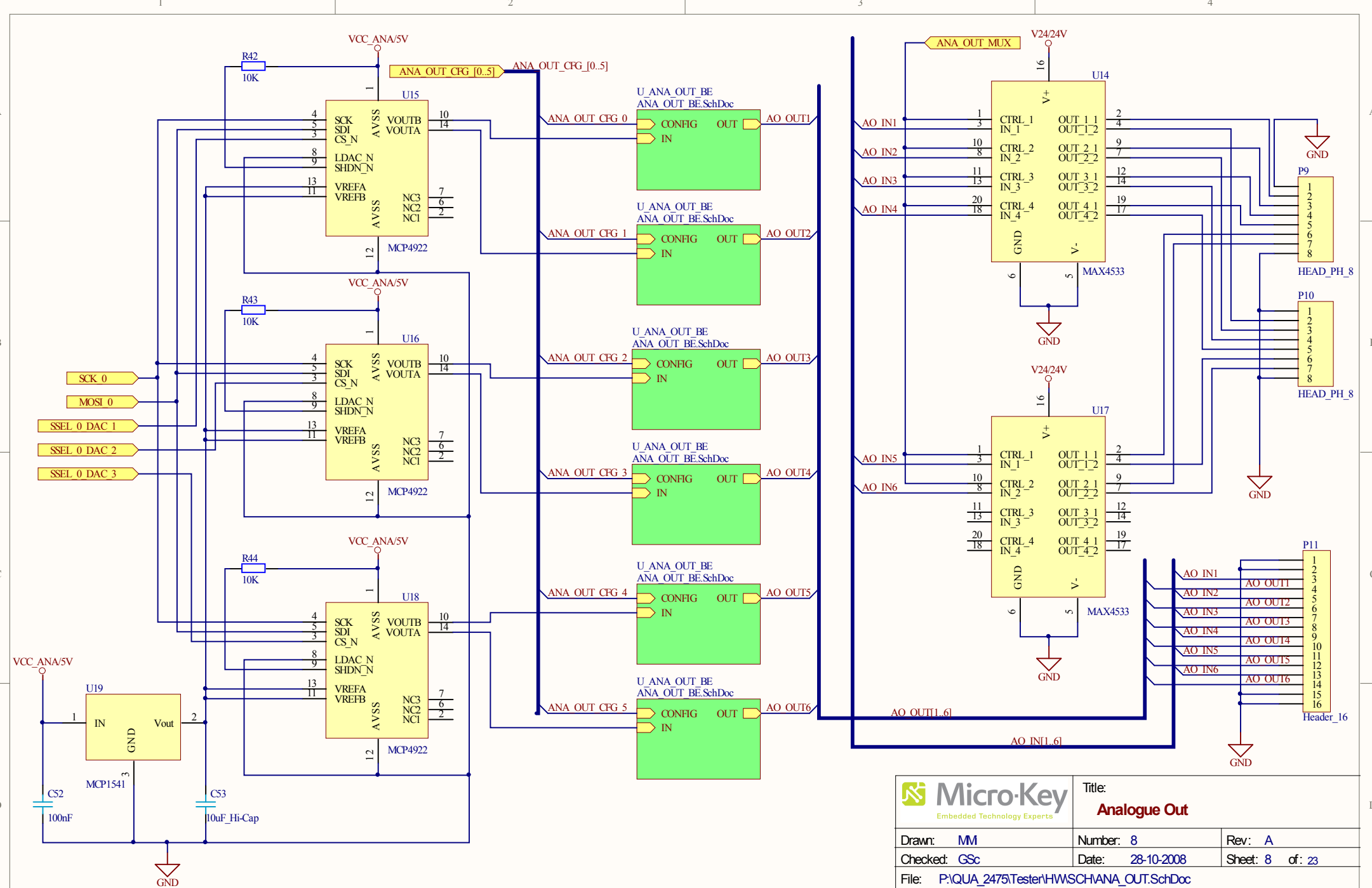
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Drawn: MM	Number: 4		Rev: A
Checked: GSc	Date: 28-10-2008		Sheet: 4 of: 23
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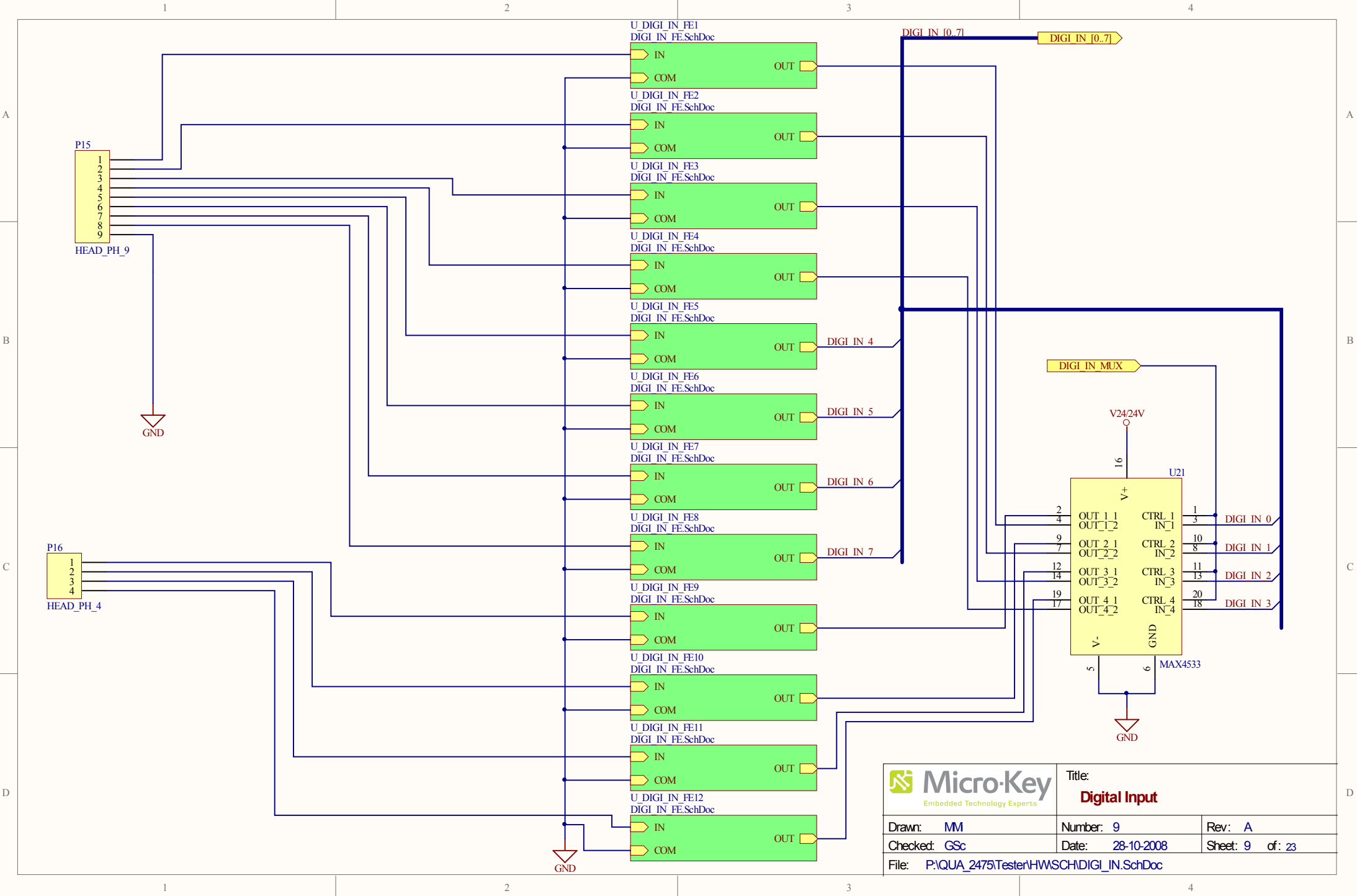


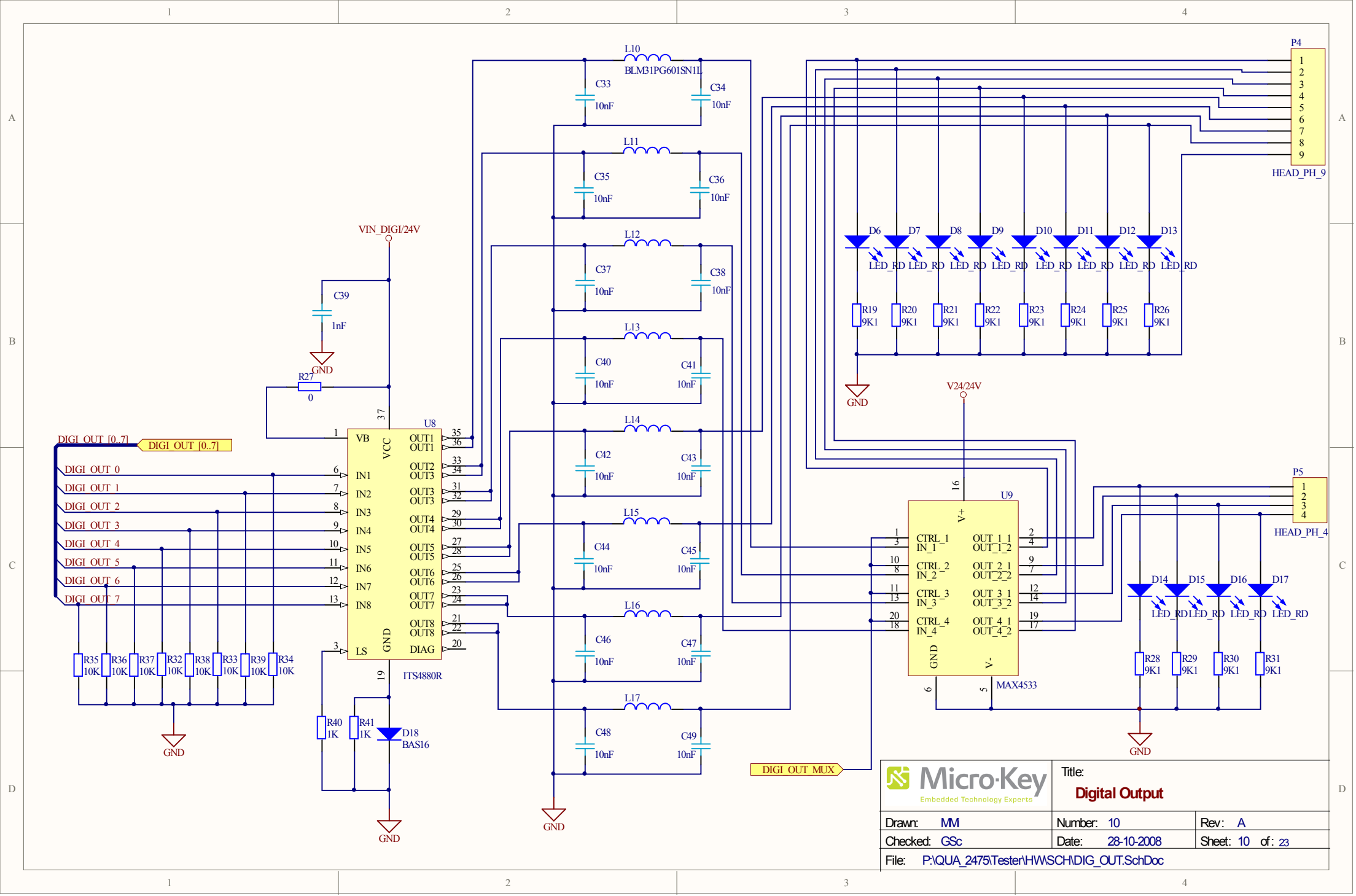
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Drawn: MM	Number: 6	Rev: A	
Checked: GSc	Date: 28-10-2008	Sheet: 6	of: 23
File: P:\QUA_2475\Tester\HMSCHLPC2378_SYS.SchDoc			





 Micro-Key Embedded Technology Experts		Title: Analogue Out	
Drawn: MM	Number: 8	Rev: A	
Checked: GSc	Date: 28-10-2008	Sheet: 8 of: 23	
File: P:\QUA_2475\Tester\HWSCH\ANA_OUT.SchDoc			





A

B

C

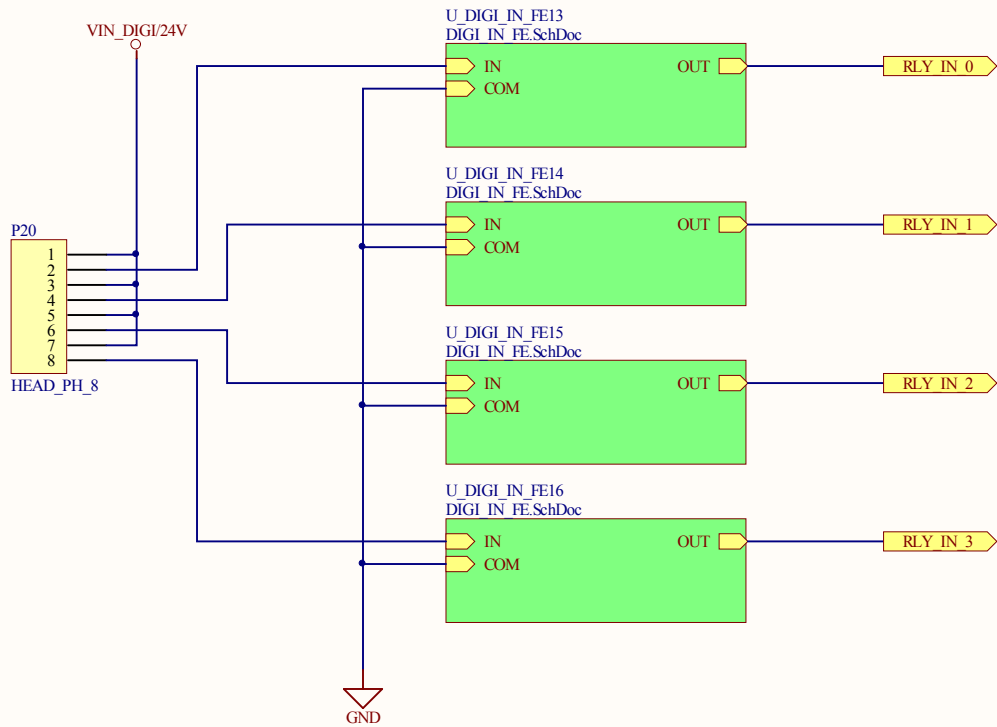
D


A

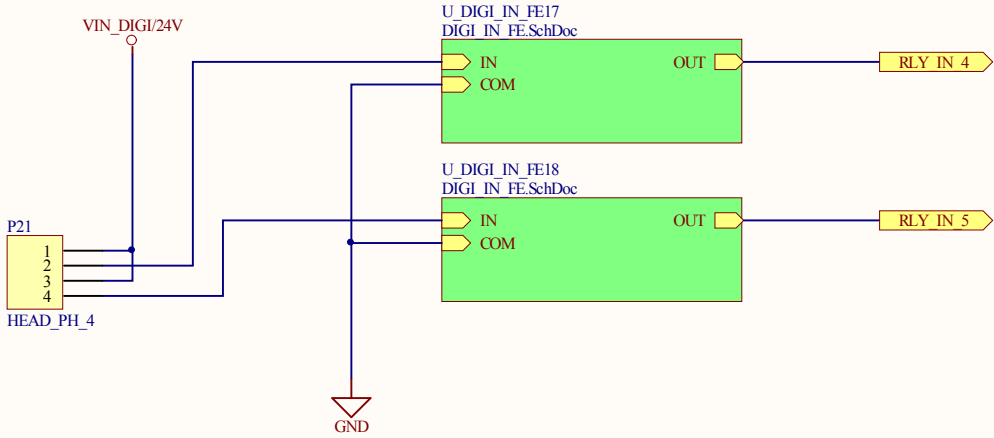
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C

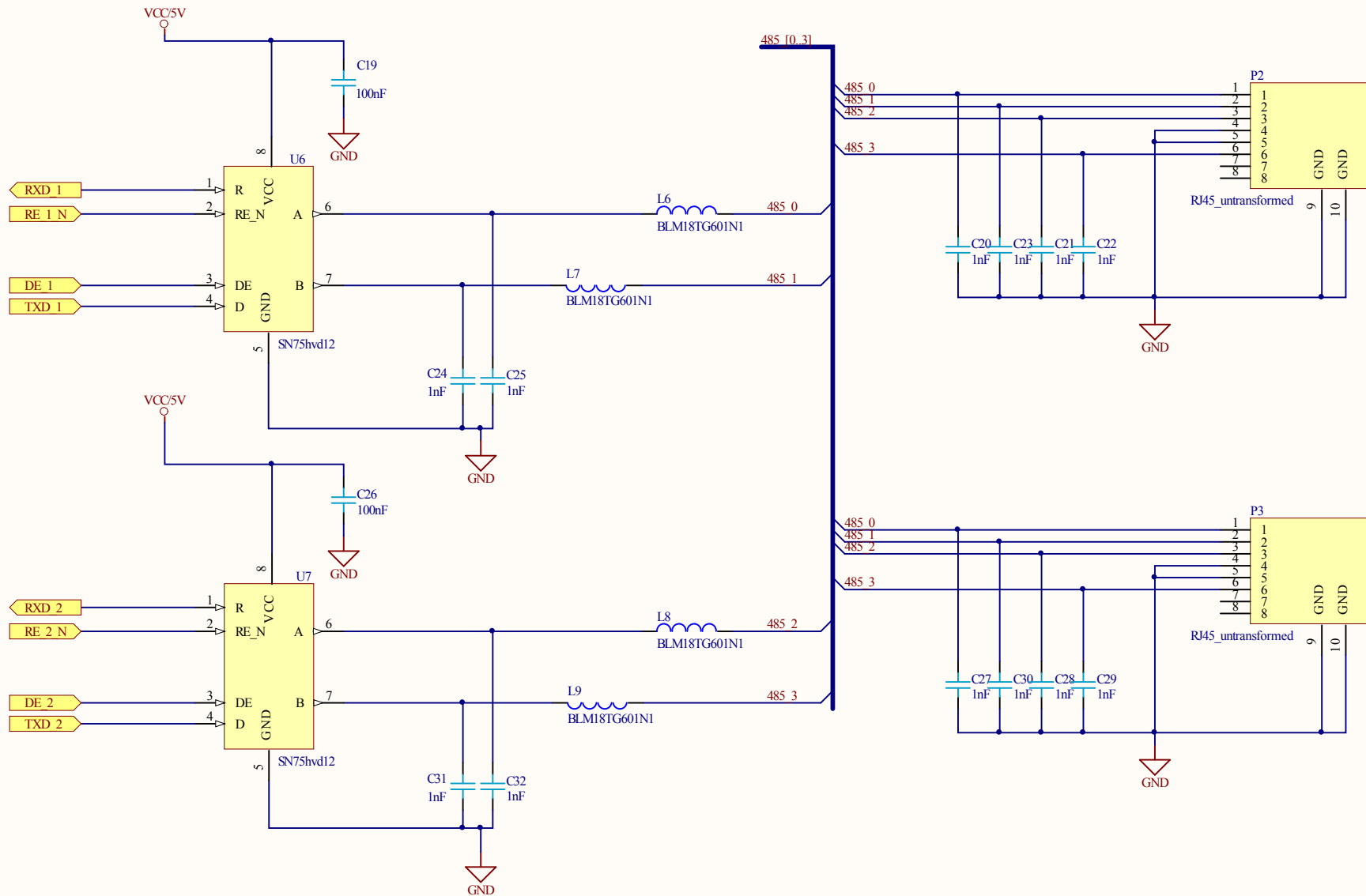
D




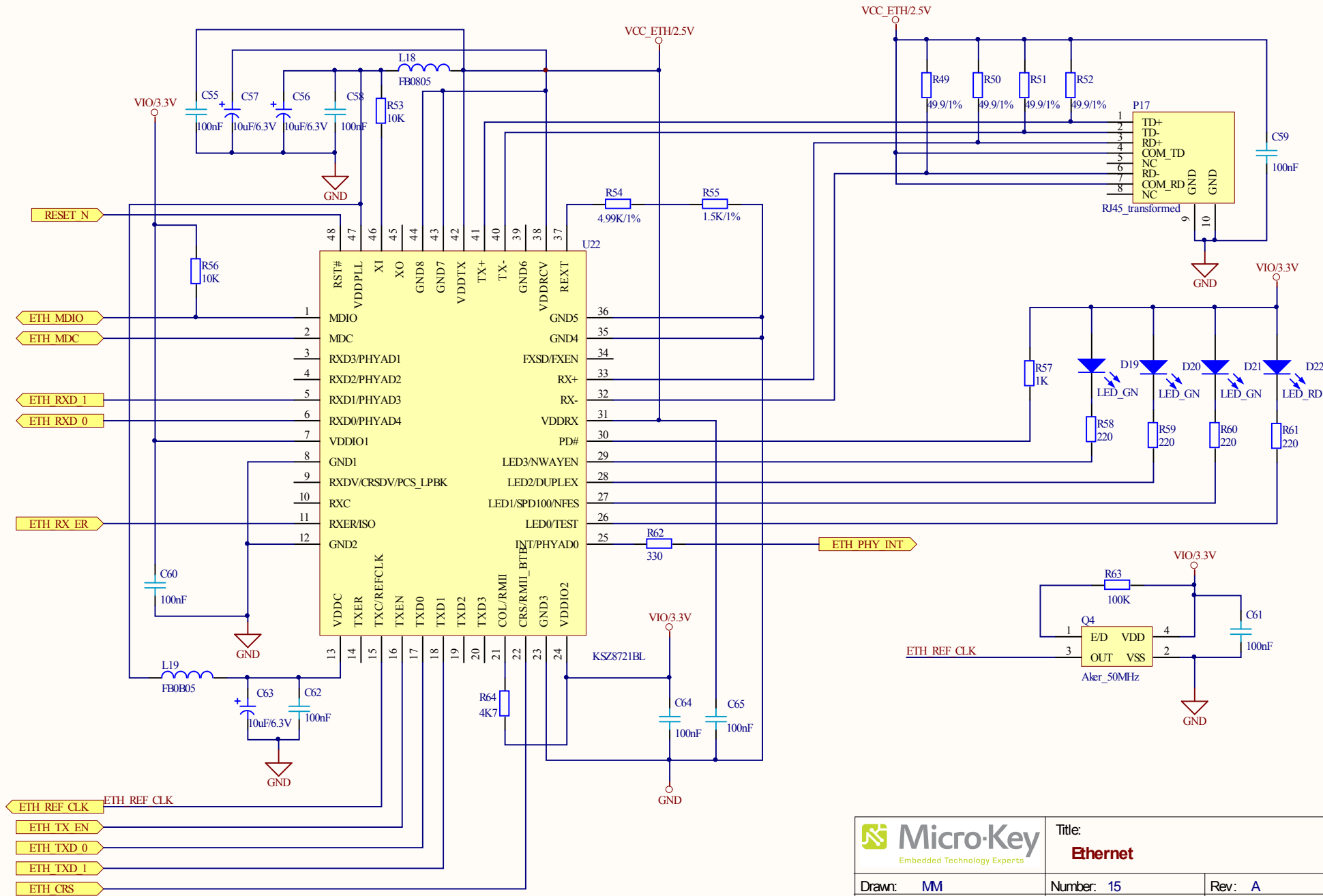
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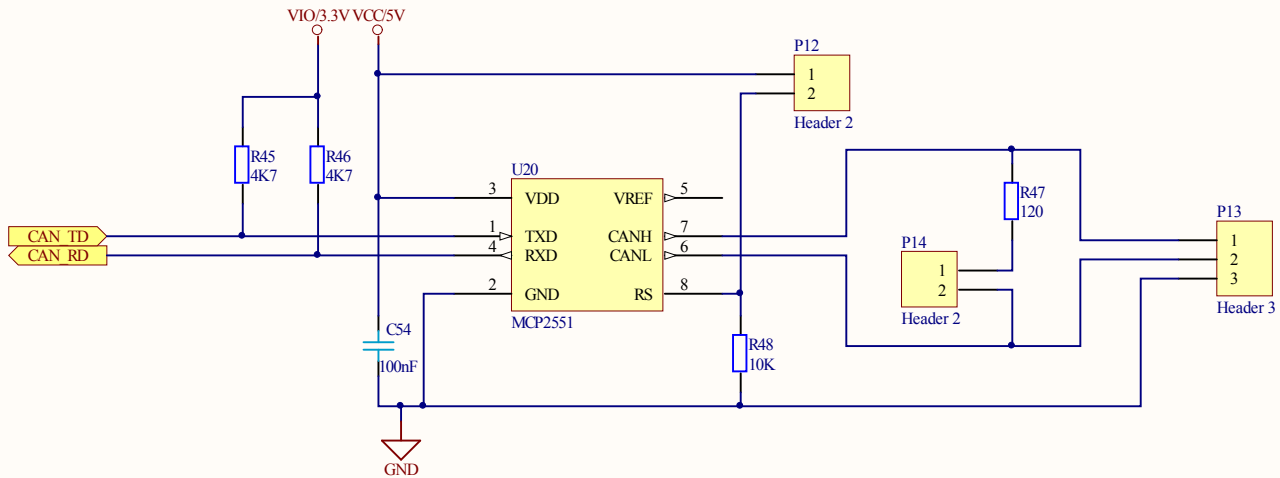



 Micro-Key Embedded Technology Experts		Title: Relay Input EB	
Drawn: MM	Number: 12	Rev: A	
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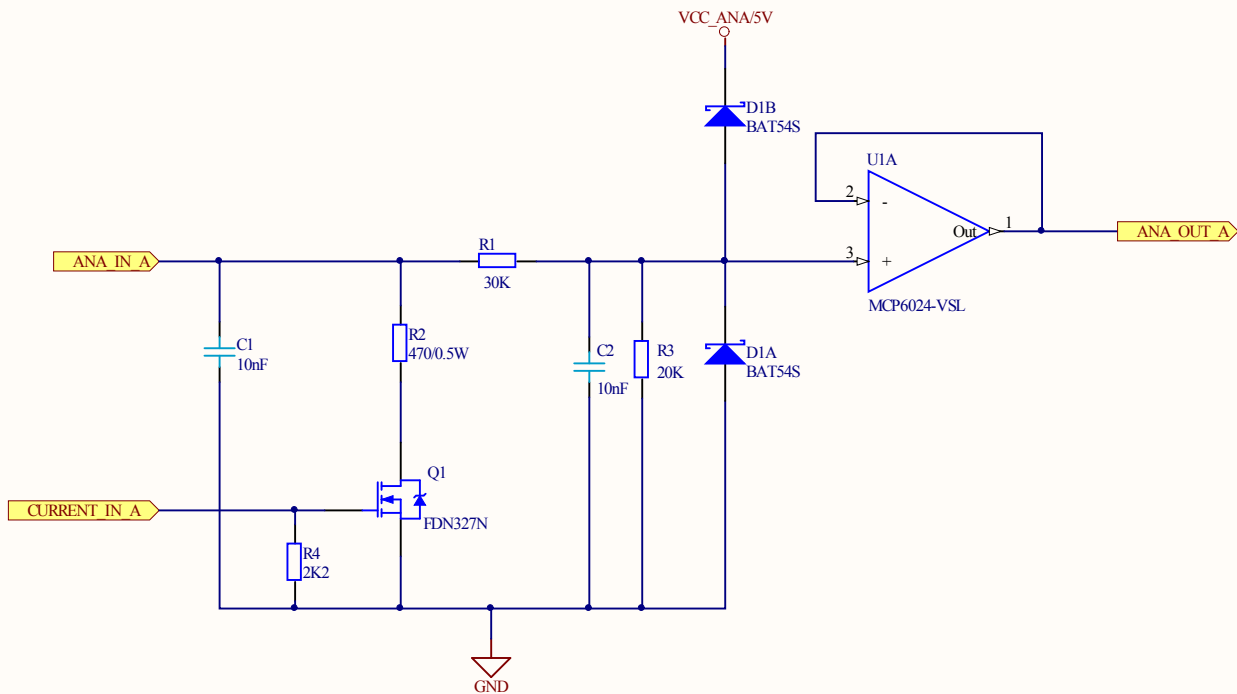



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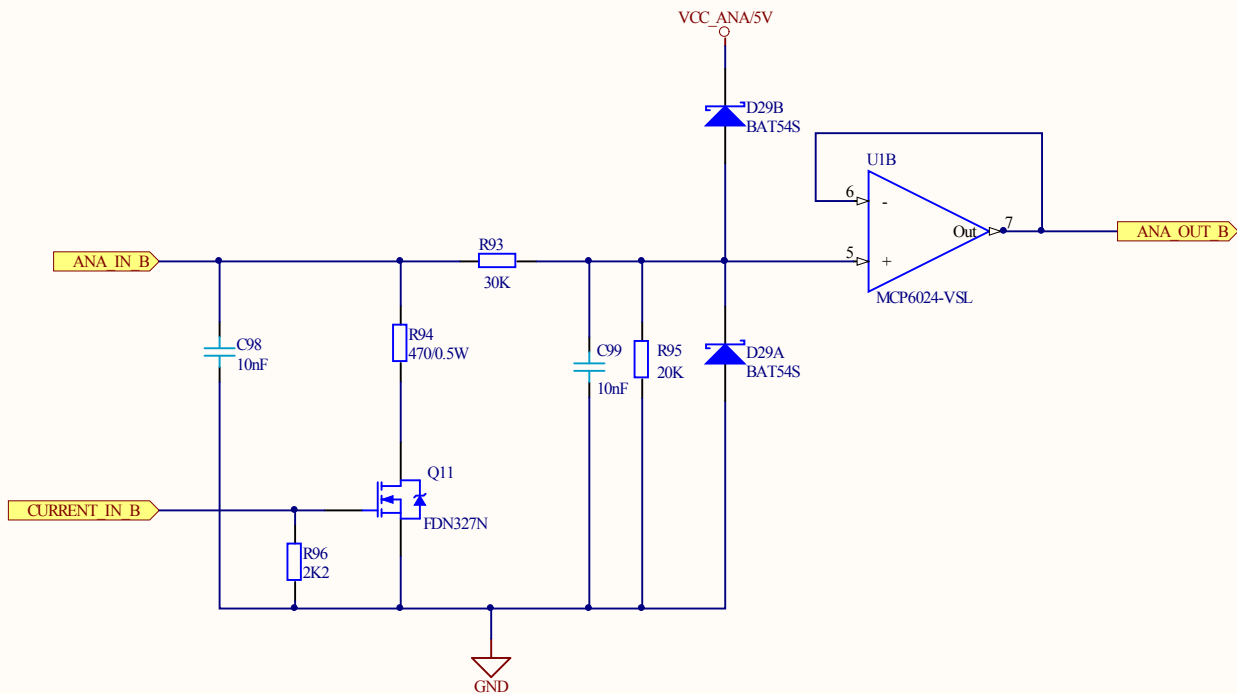





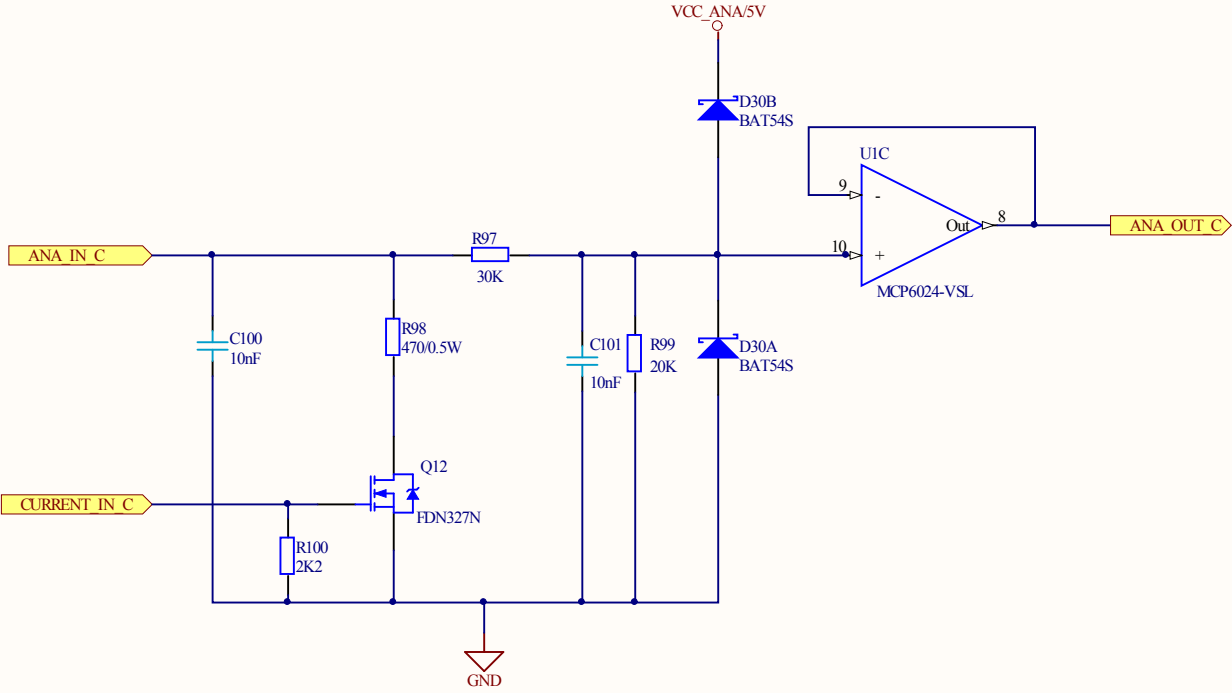
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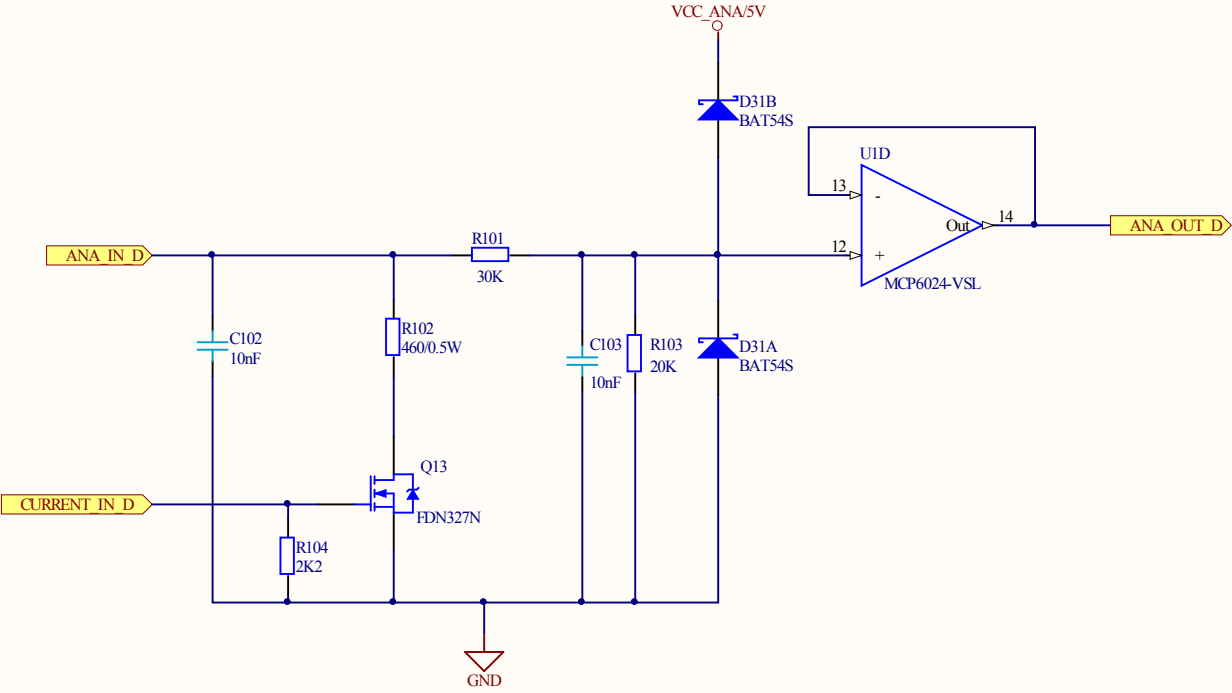
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Drawn: MM	Number: 18	Rev: A	
Checked: GSc	Date: 28-10-2008	Sheet: 18 of: 23	
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 Micro-Key Embedded Technology Experts		Title: Analogue In FE_B	
Drawn: MM	Number: 19	Rev: A	
Checked: GSc	Date: 28-10-2008	Sheet: 19 of: 23	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_B.SchDoc			



 Embedded Technology Experts	Title: Analogue In FE_C	
Drawn: MM	Number: 20	Rev: A
Checked: GSc	Date: 28-10-2008	Sheet: 20 of: 23
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_C.SchDoc		



Title:
Analogue In FE_D

Drawn: MM

Number: 21

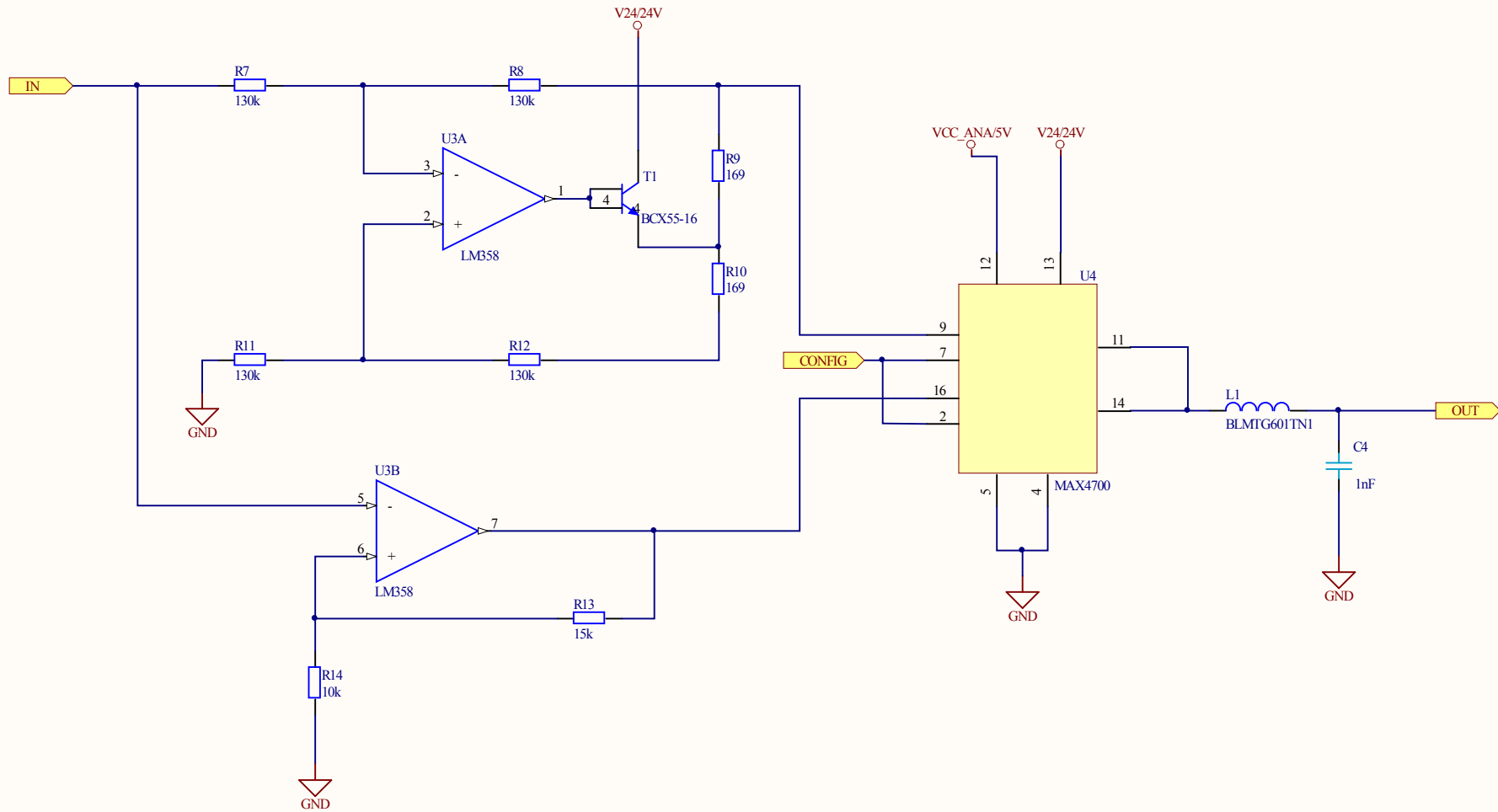
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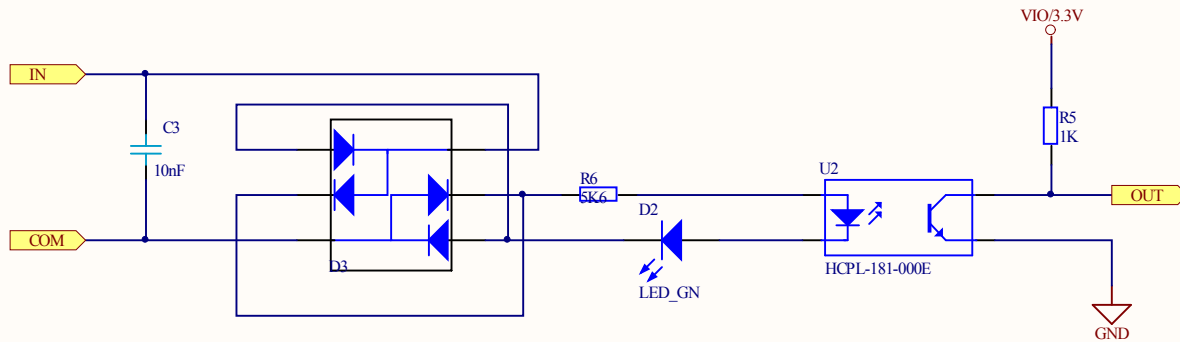
Date: 28-10-2008


Sheet: 21 of: 23

File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_D.SchDoc



 Micro-Key Embedded Technology Experts		Title: *	
Drawn: *		Number: 22	Rev: *
Checked: *		Date: 28-10-2008	Sheet: 22 of: 23
File: P:\QUA_2475\Tester\HWSCHANA_OUT_BE.SchDoc			



 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 23	Rev: A	
Checked: GSc	Date: 28-10-2008	Sheet: 23 of: 23	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			